

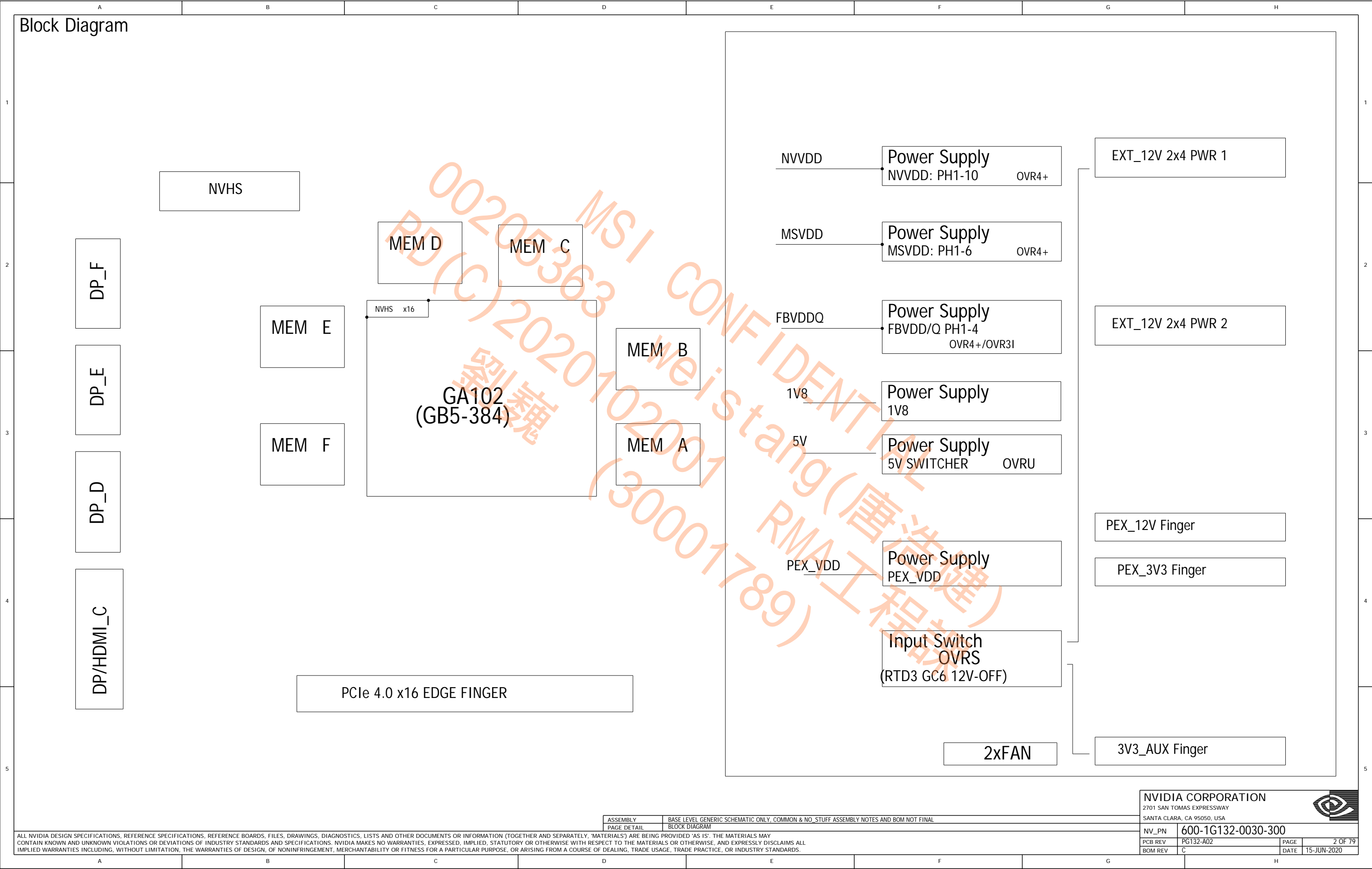
PG132-A02

320W, FH Std PCB, 320b, GDDR6X 2CH x16  
DP + DP + DP + HDMI

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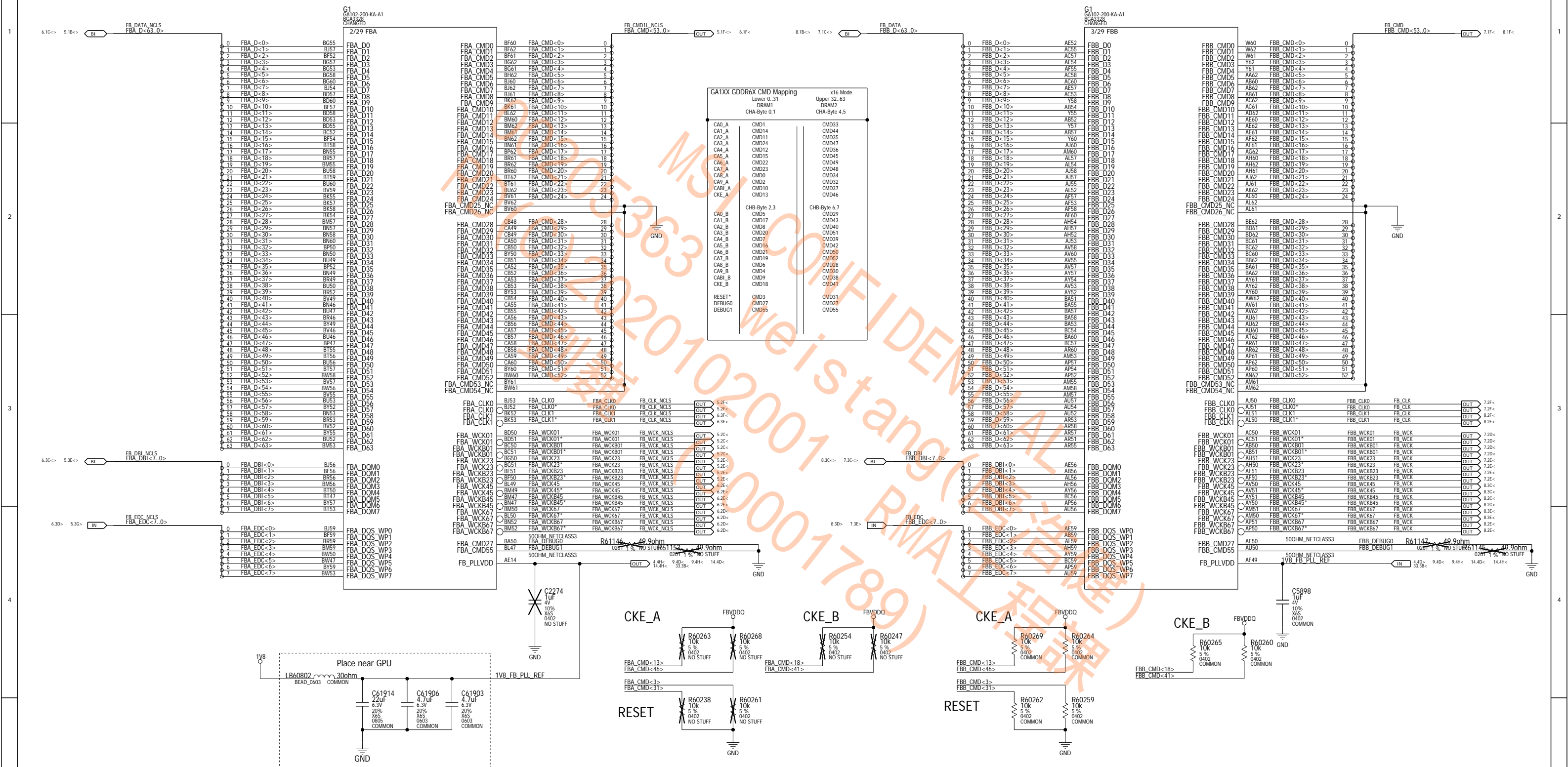
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MEMORY: GPU Partition A/B



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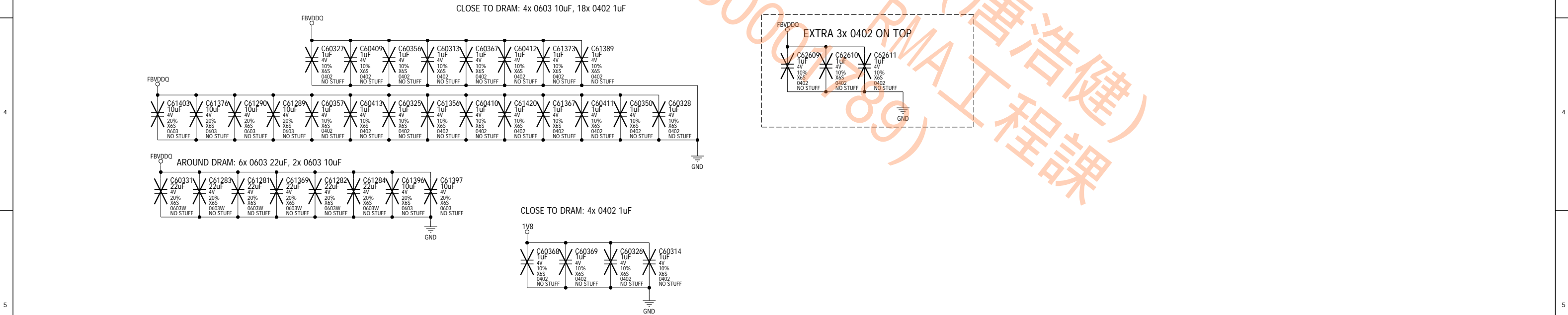
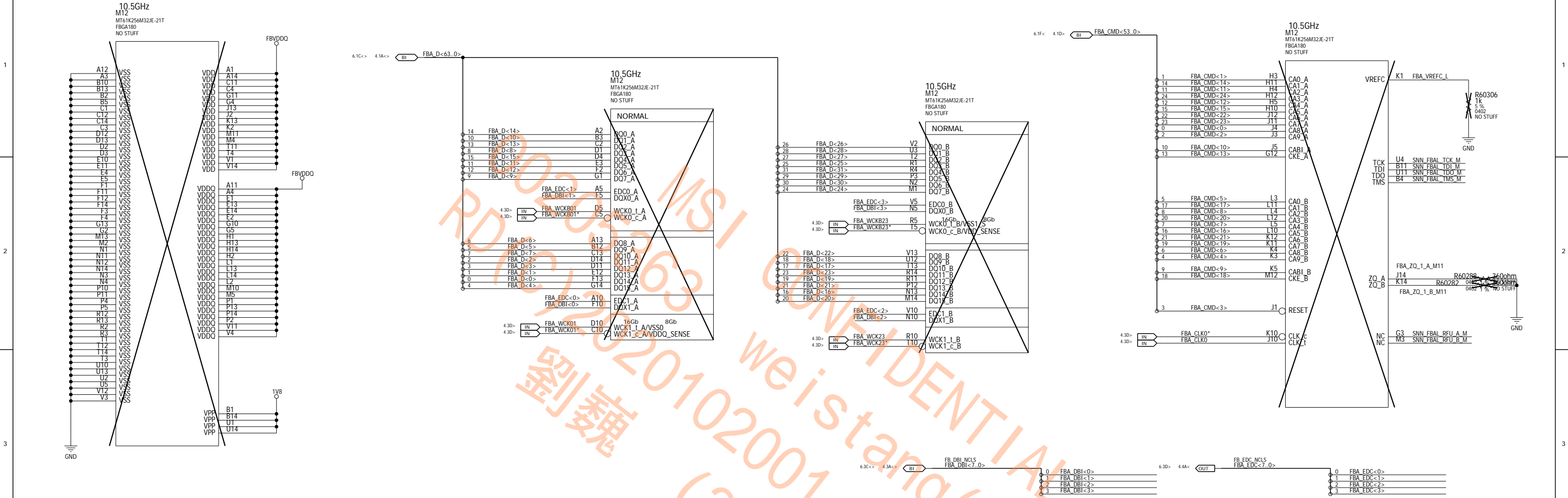
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PAGE DETAIL	MEMORY: GPU PARTITION A/B

MEMORY: FBA Partition 31..0

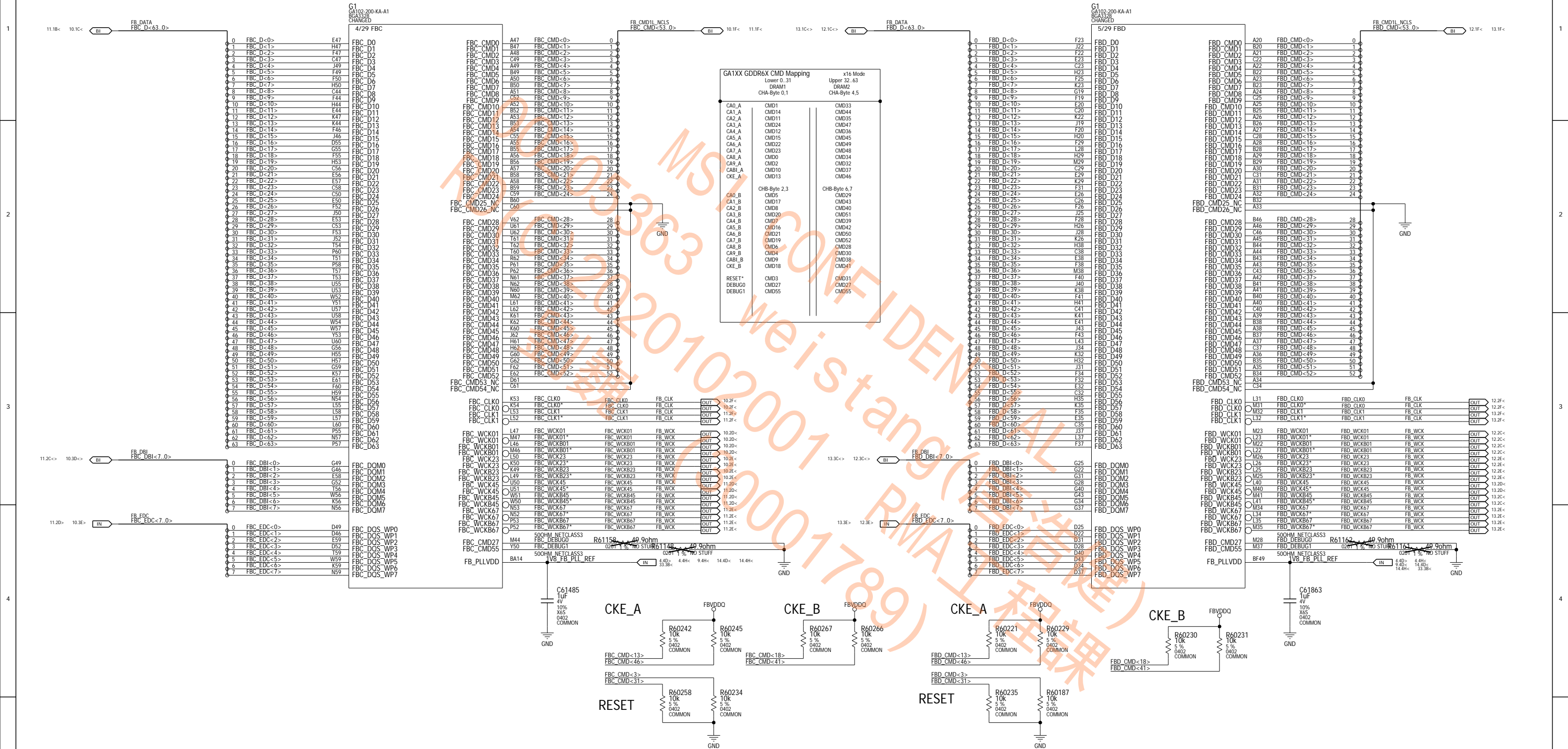








MEMORY: GPU Partition C/D

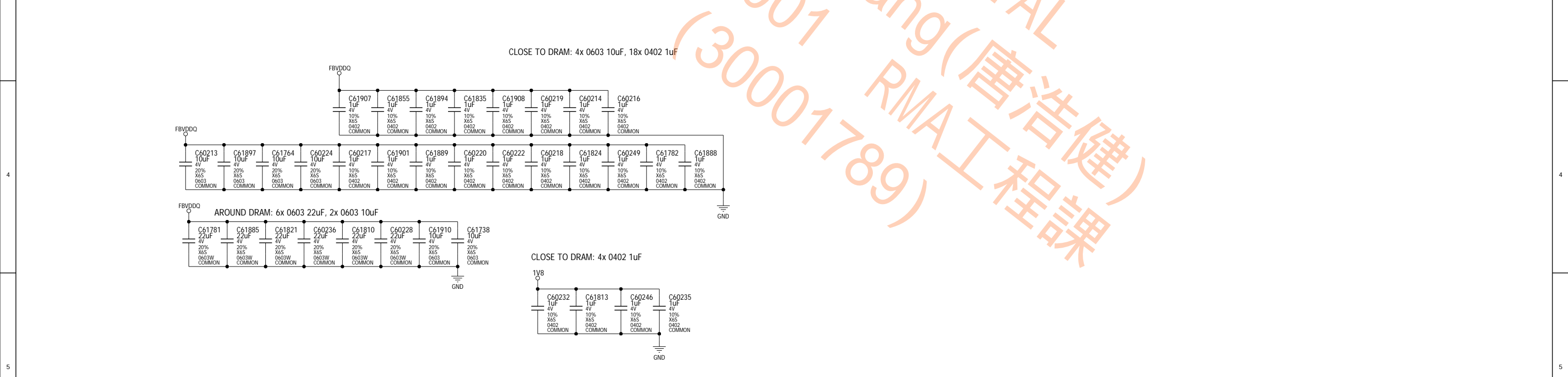
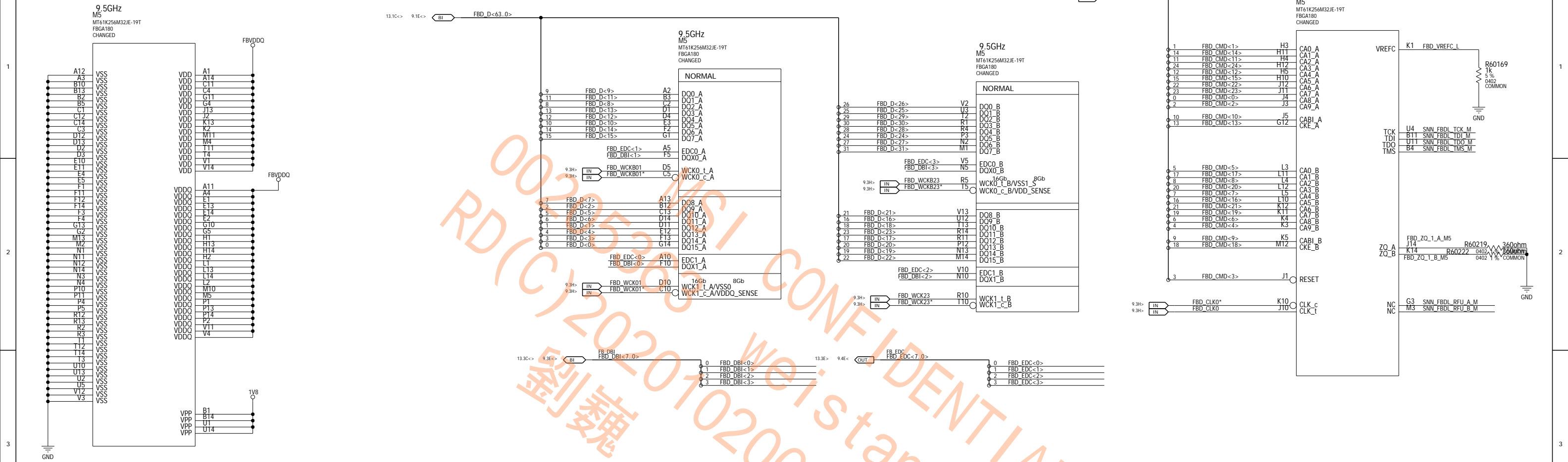


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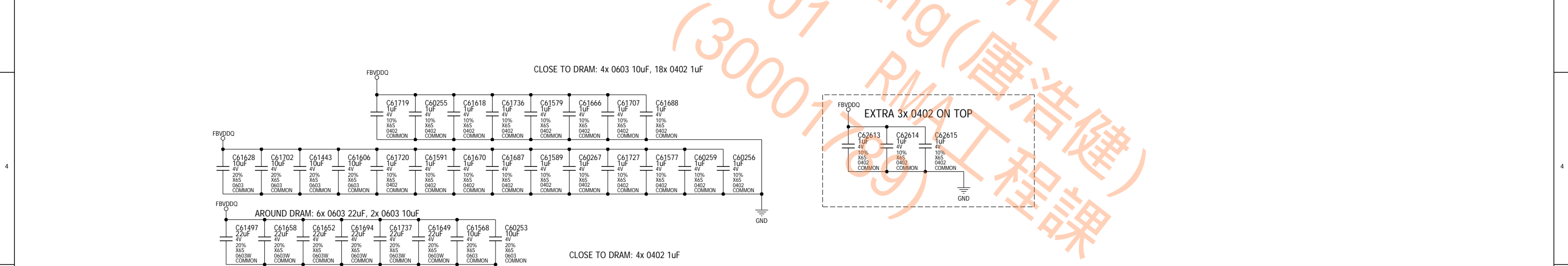
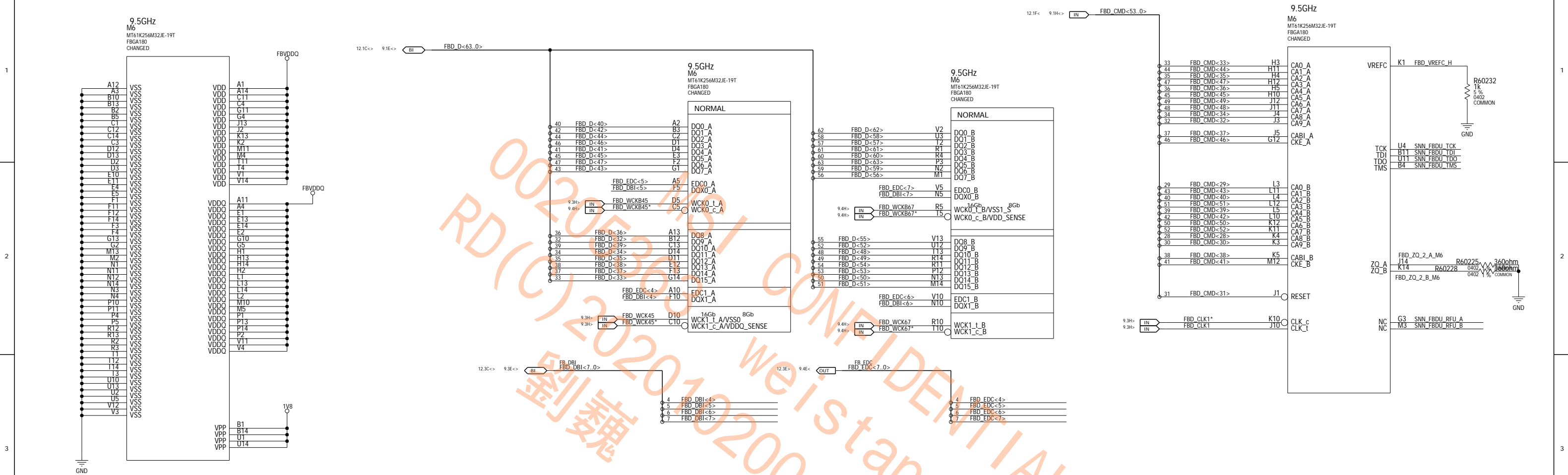




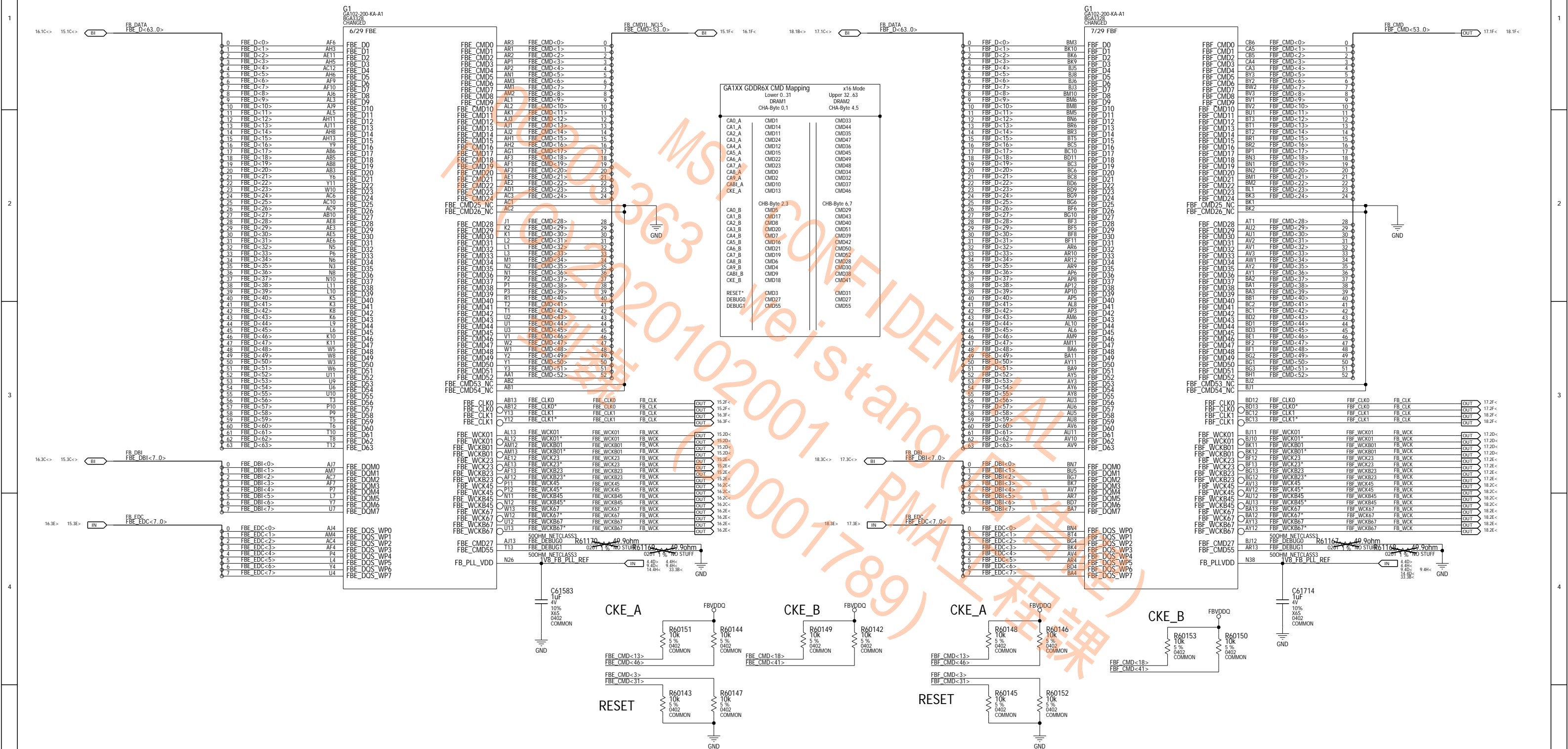
MEMORY: FBD Partition 31..0



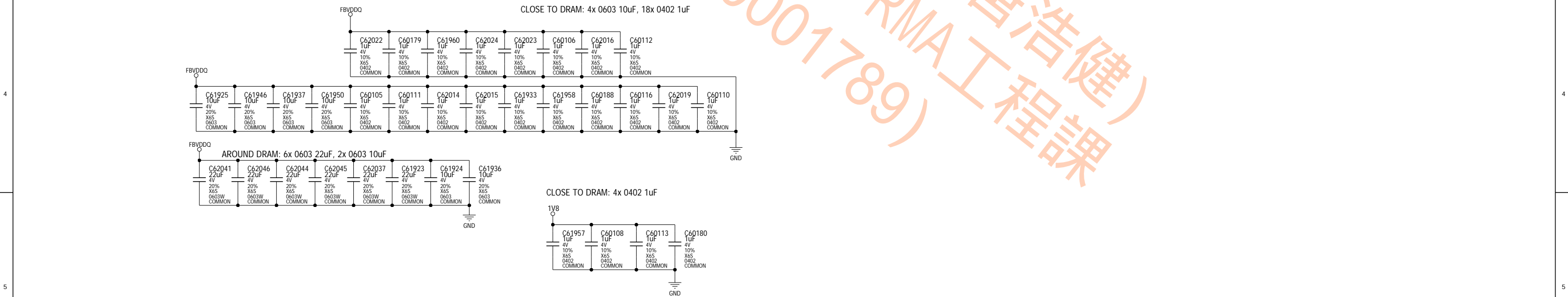
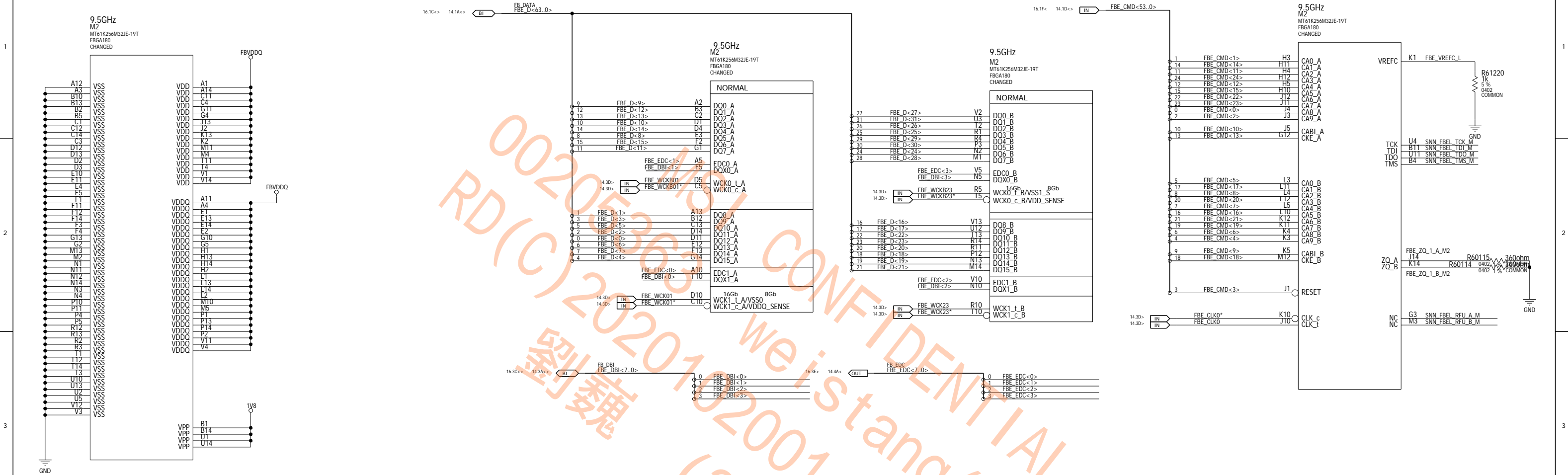
MEMORY: FBD Partition 63..32



MEMORY: GPU Partition E/F

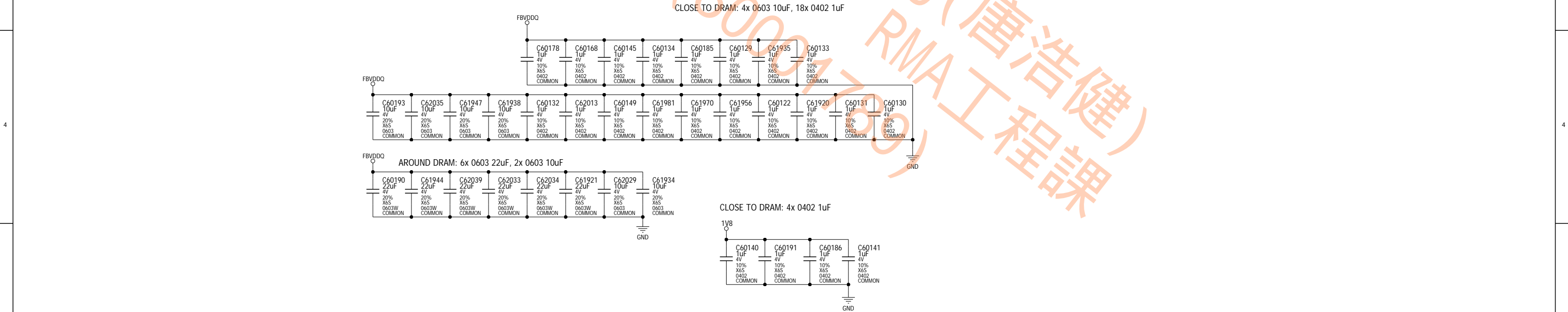
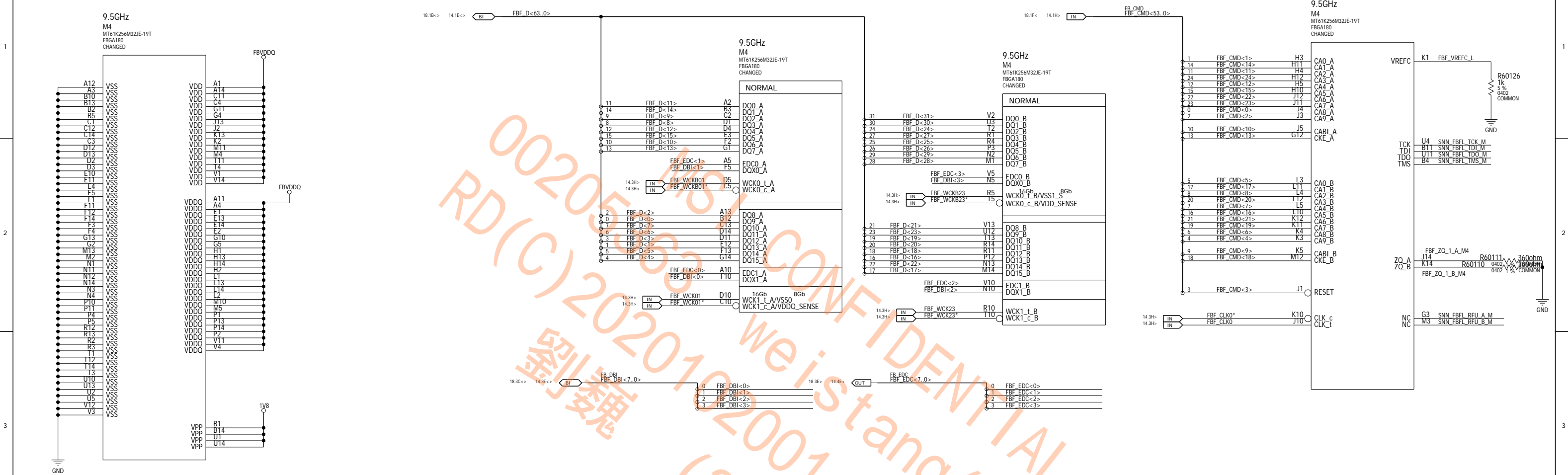


MEMORY: FBE Partition 31..0





MEMORY: FBF Partition 31..0





GPU GND, RFUs & RSVD



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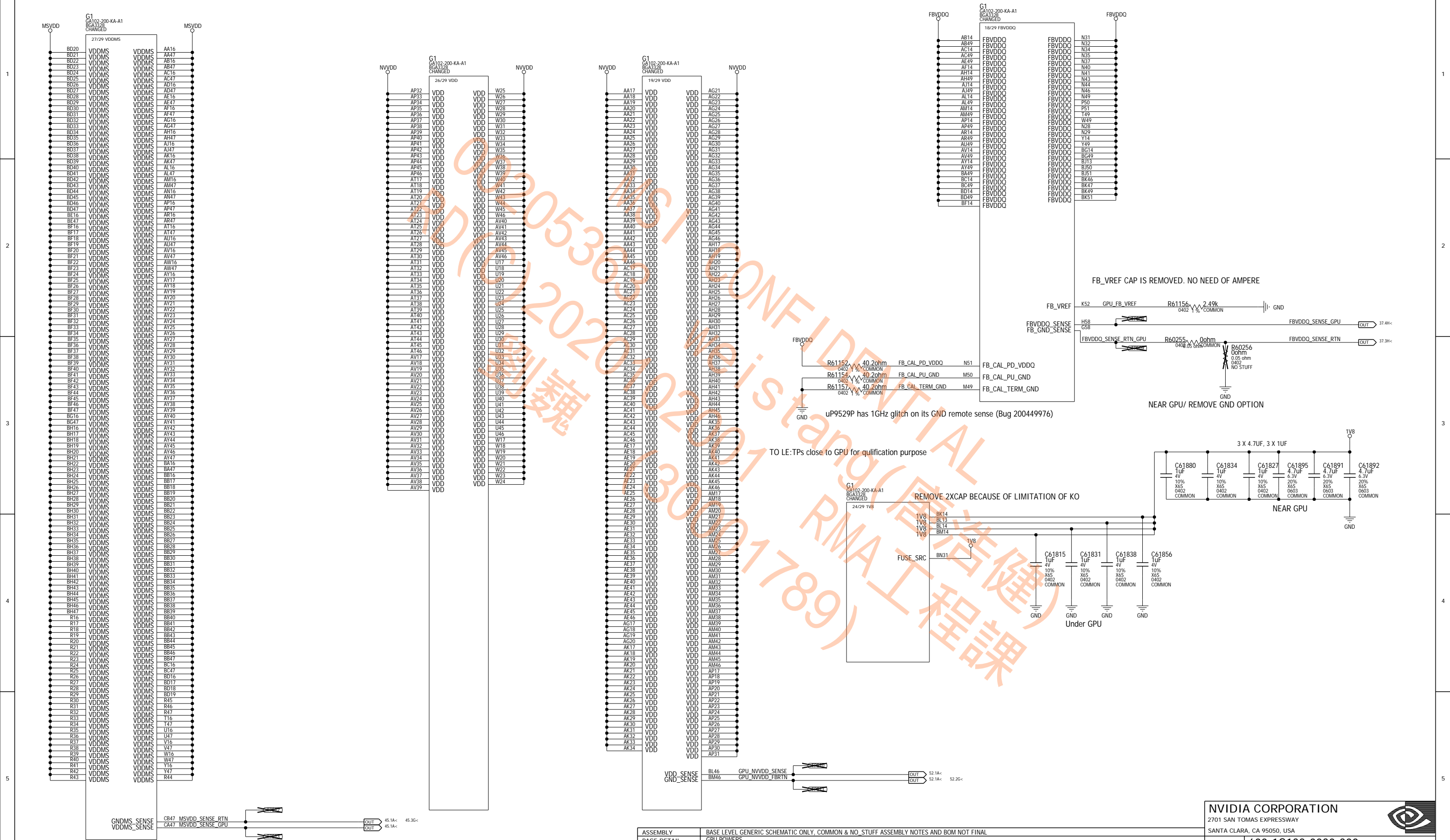
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PAGE DETAIL	GPU GND, RFUS & RSVD

## GPU PWR and GND

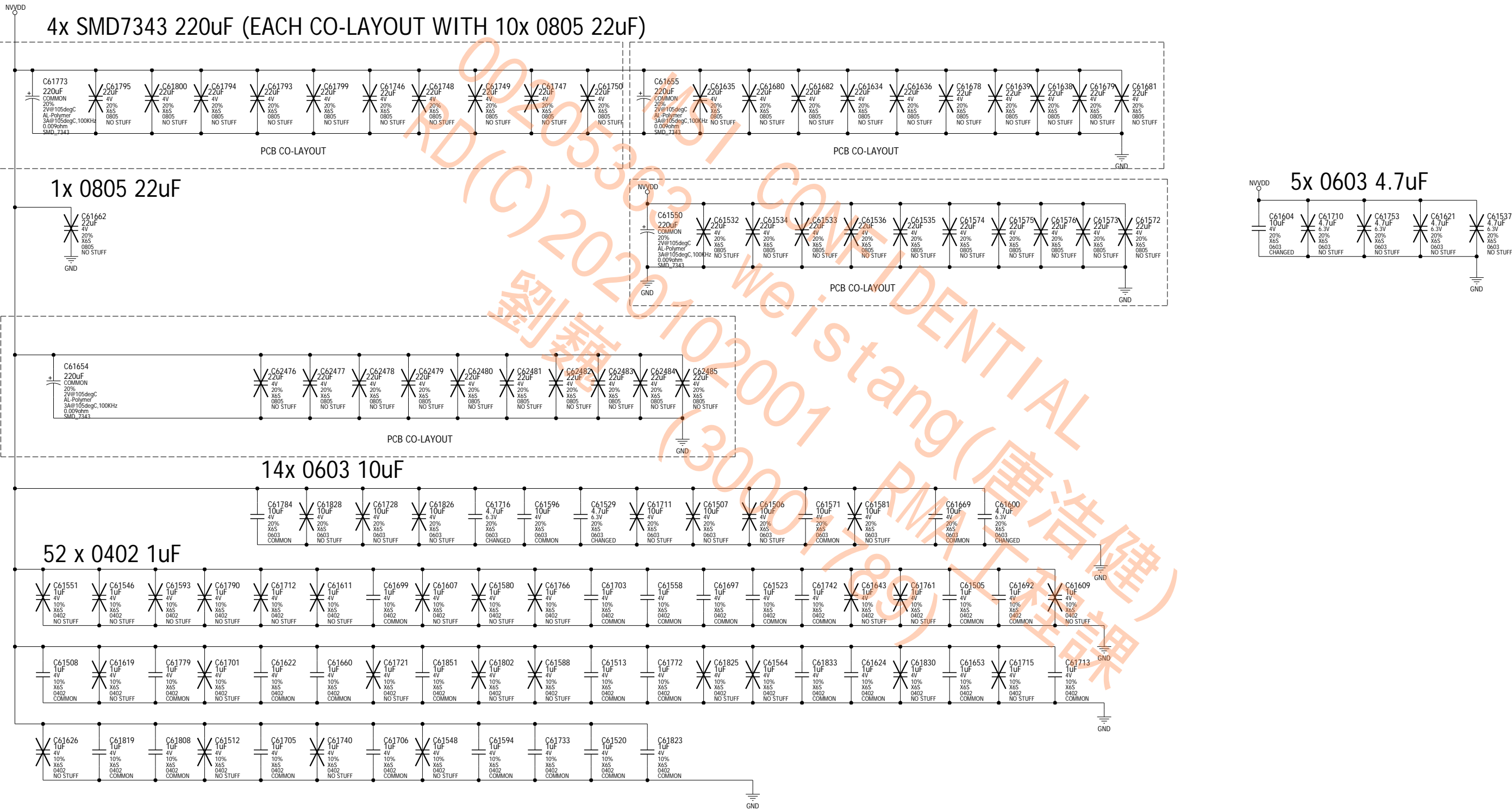


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PAGE DETAIL	GPU POWERS

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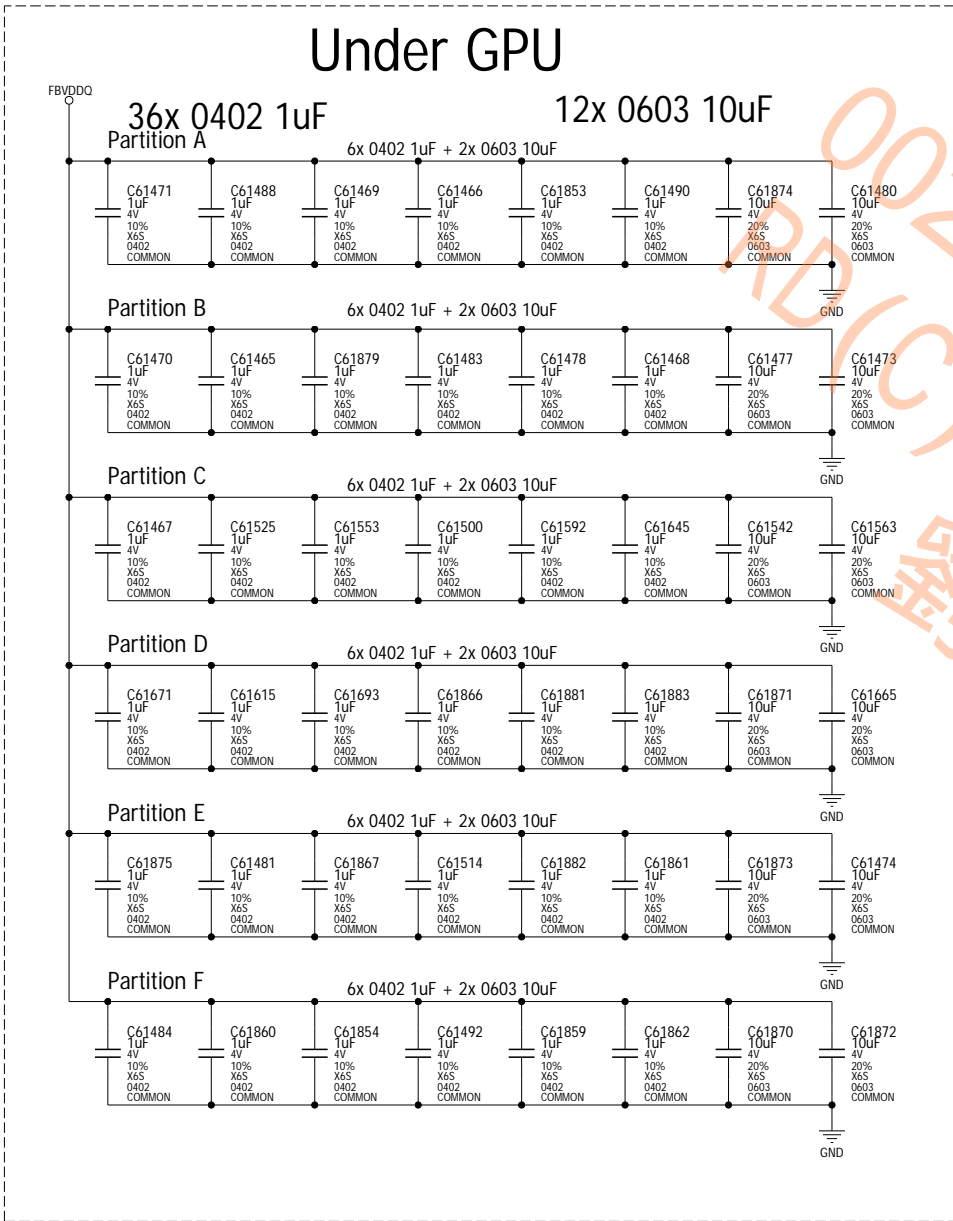
# NVVDD UNDER GPU



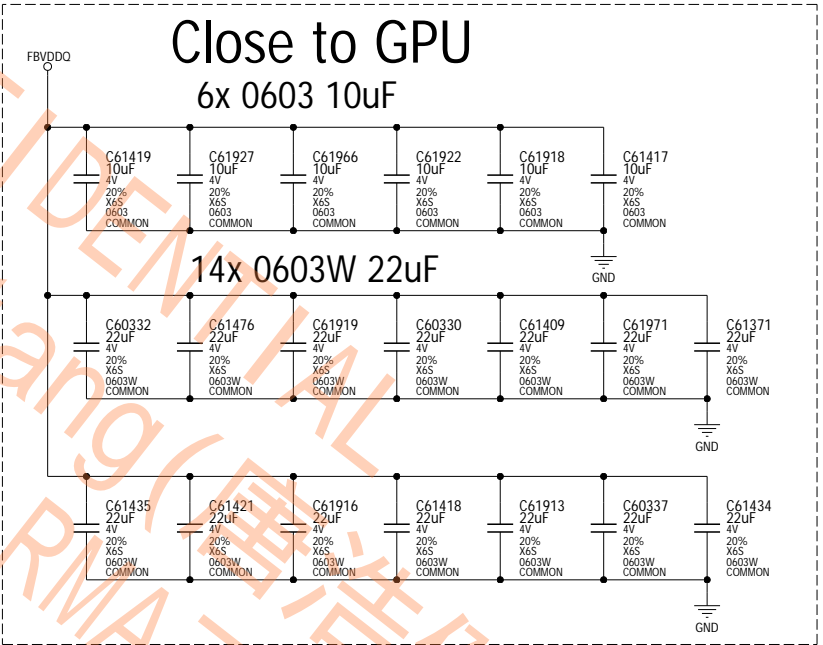
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FBVDDQ

Under GPU

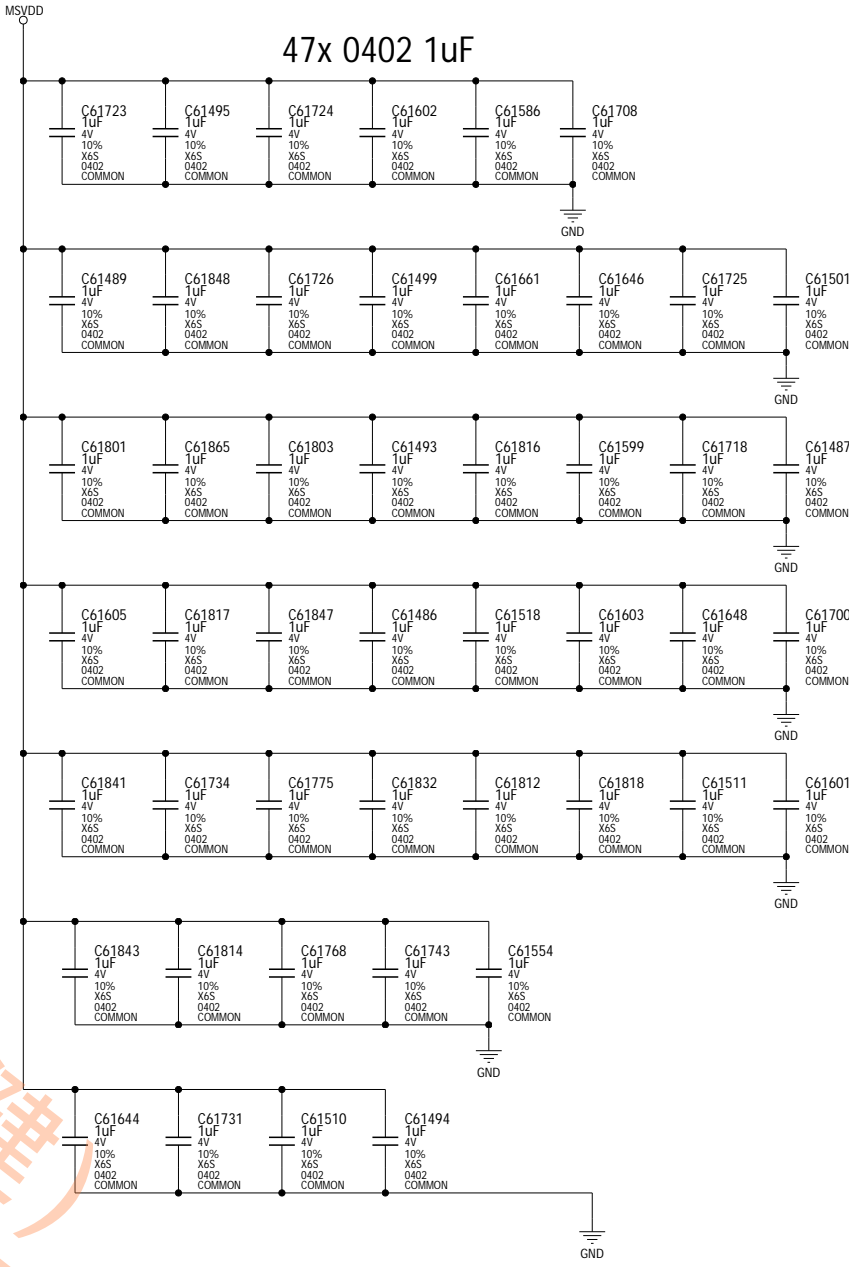
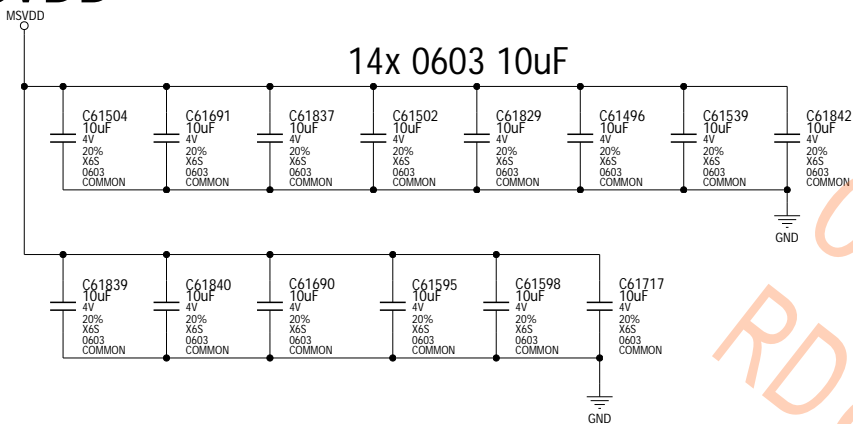


Close to GPU

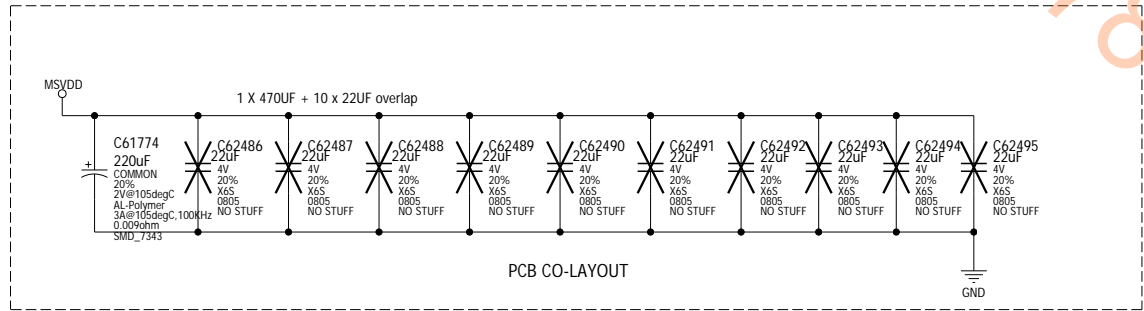
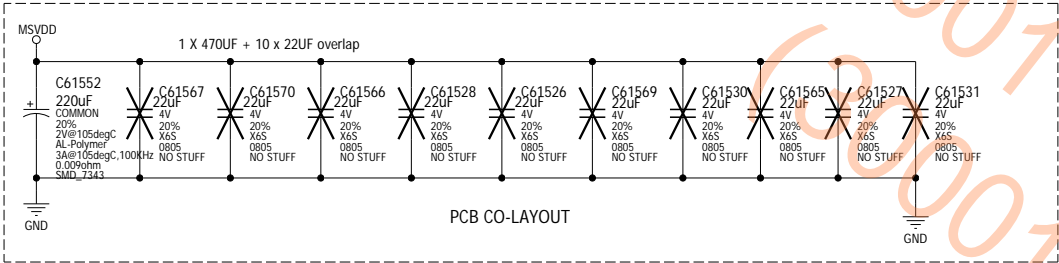


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# UNDER GPU



2x SMD7343 220uF (EACH CO-LAYOUT WITH 10x 22uF 0805)



REMOVED 3X 1uF 0402 DUE TO SPACE RESTRICTION

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5								

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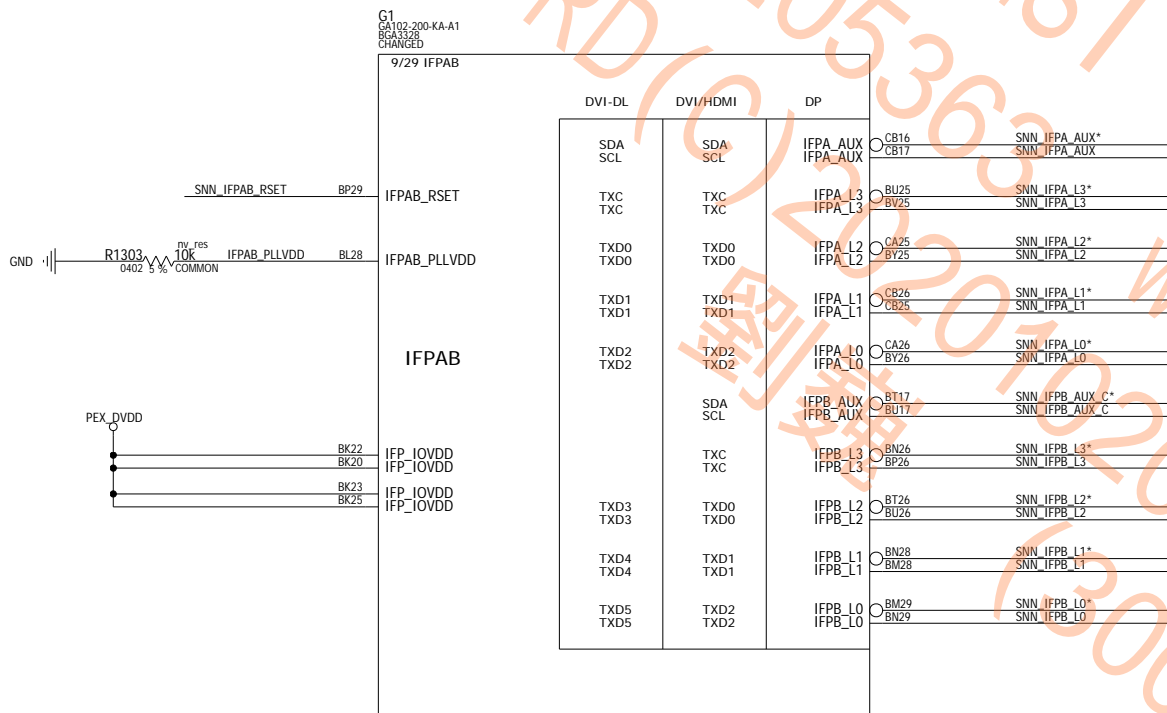
NVHS Interface and FRAME LOCK



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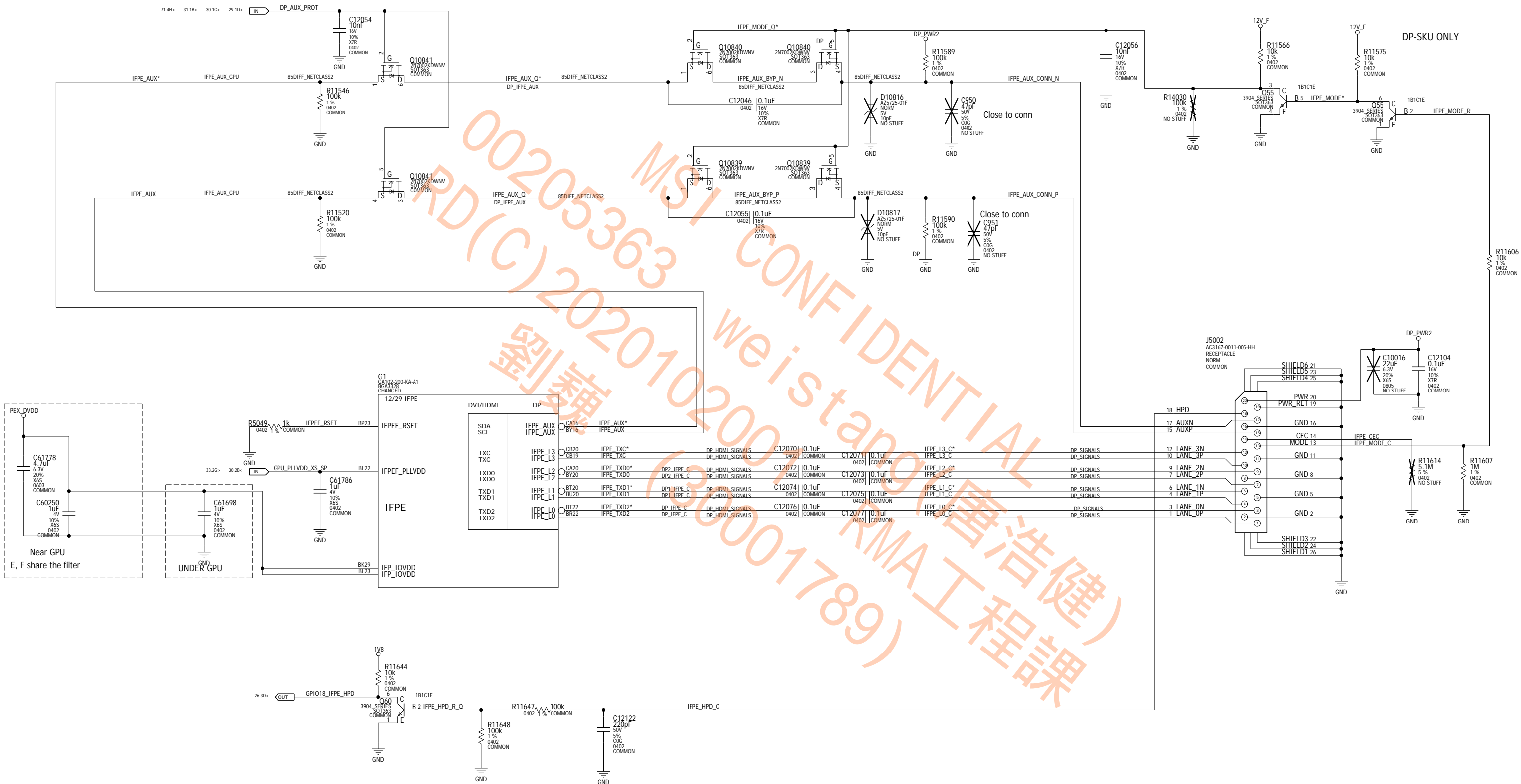


IFPA UNUSED, IFPB UNUSED



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IFPE DP



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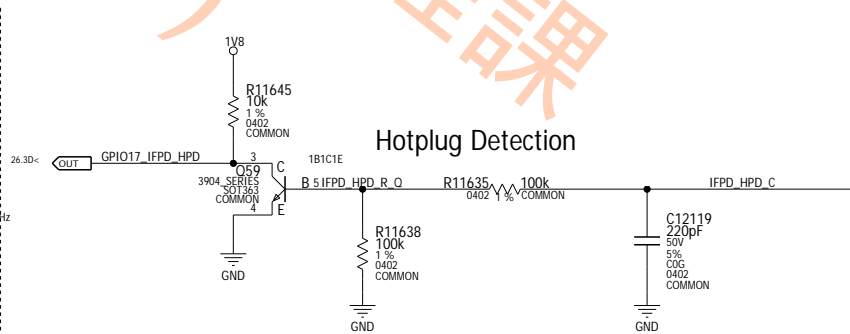
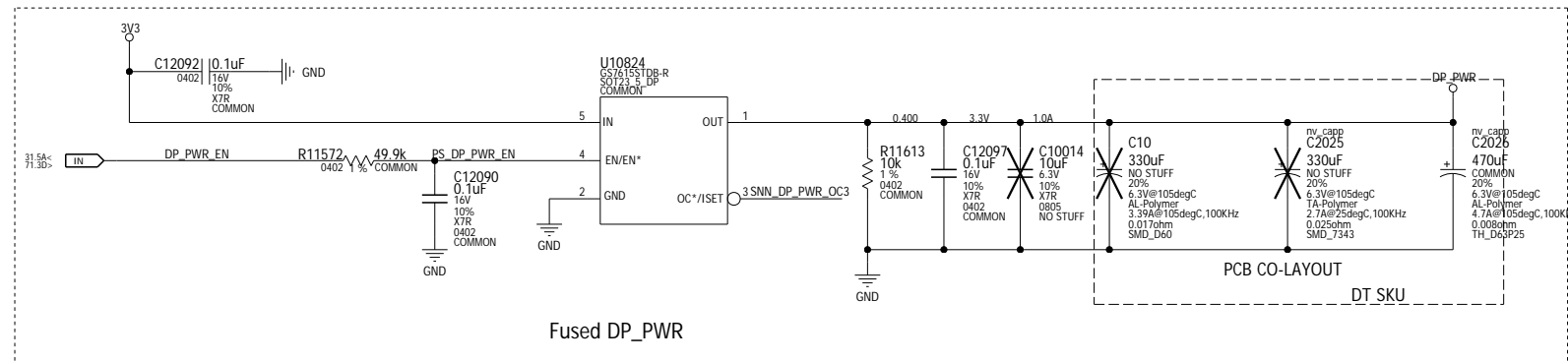
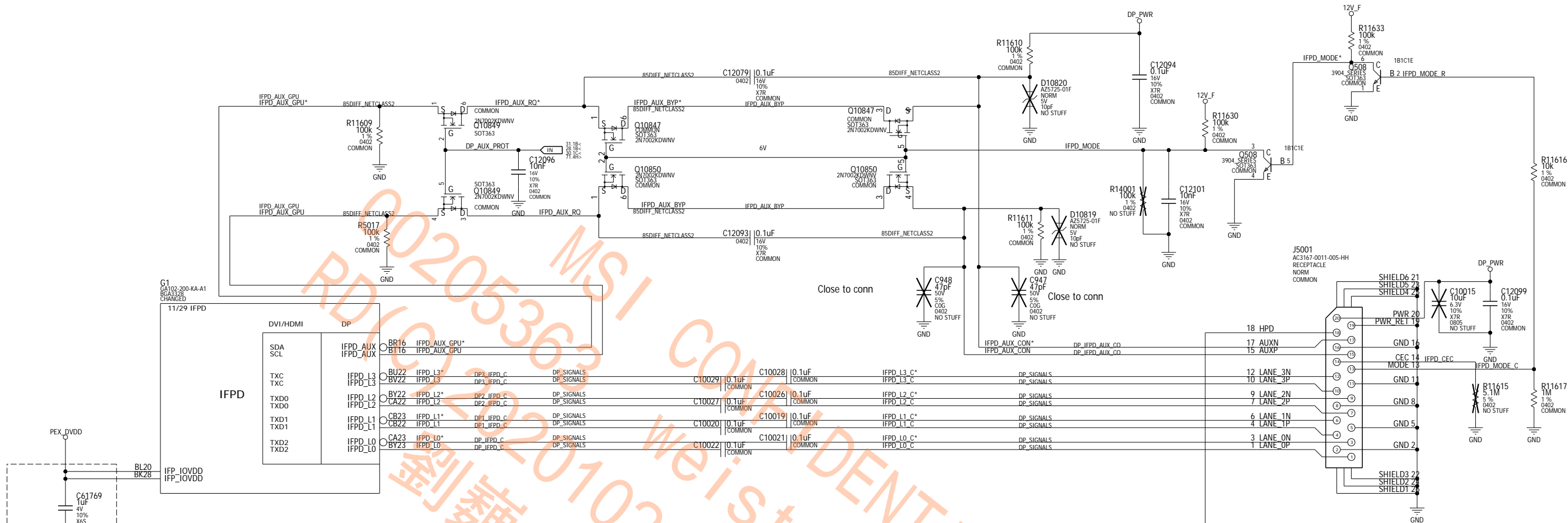
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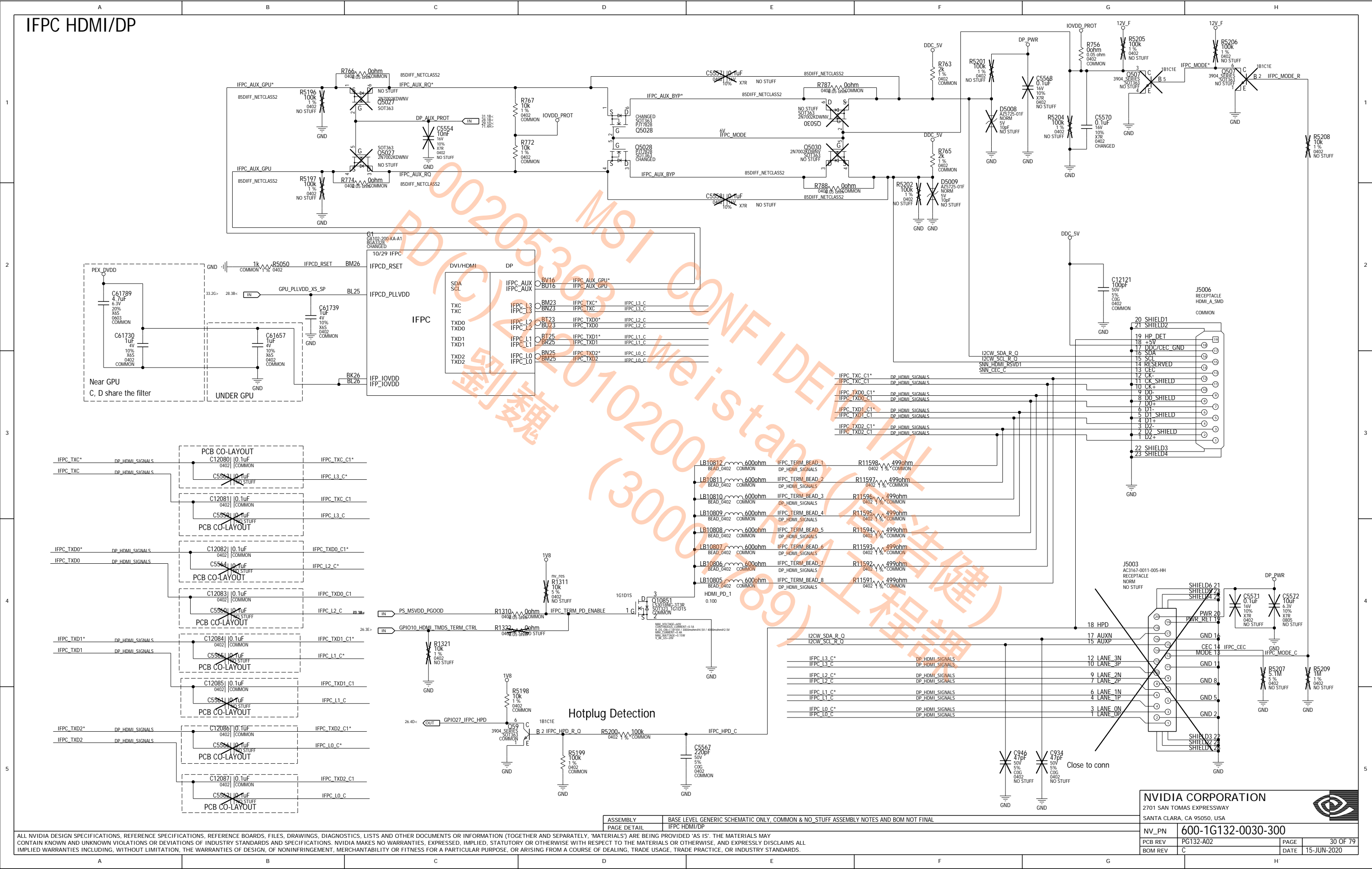
## IFPD DP



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## IFPC HDMI/DP

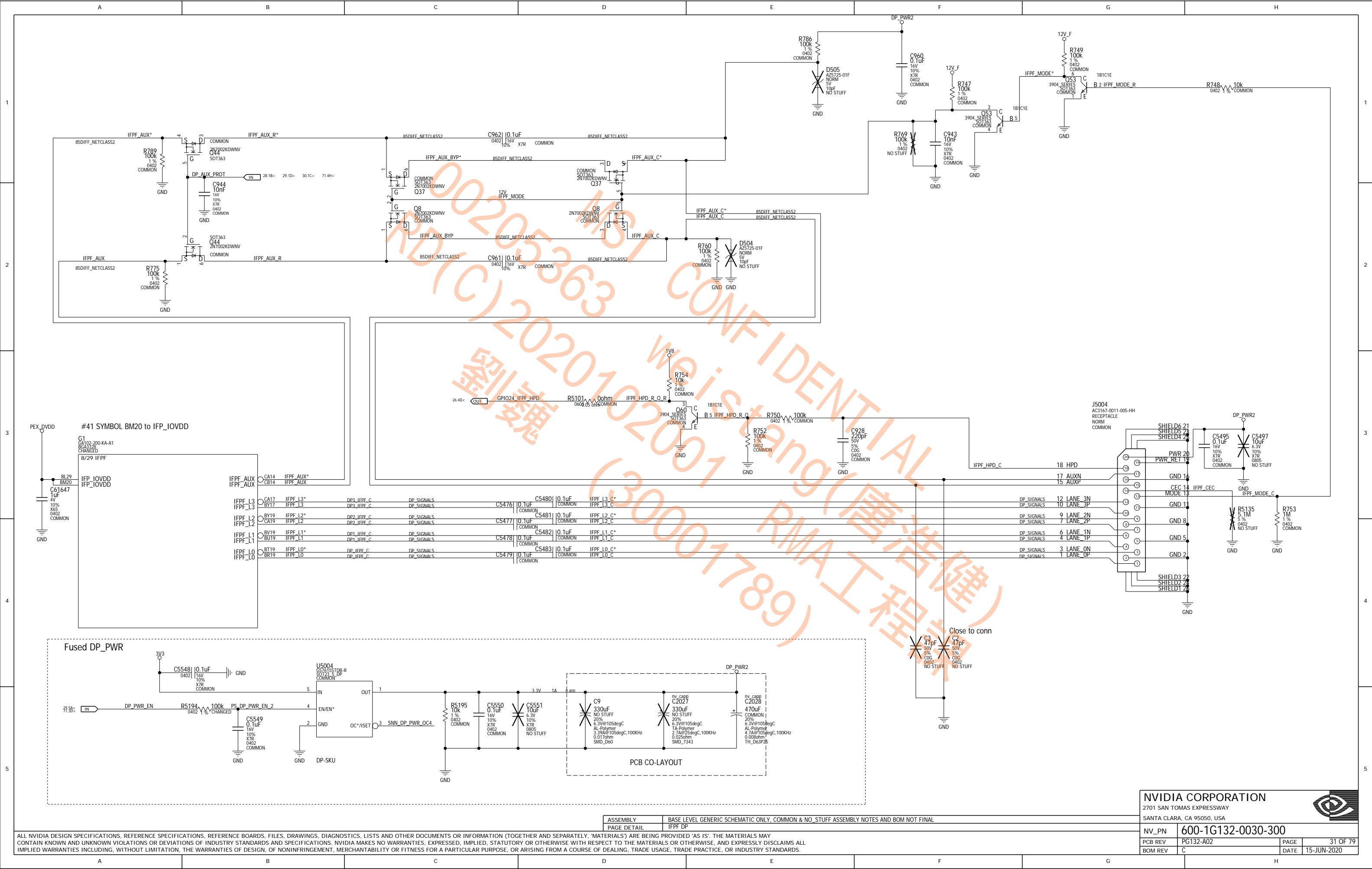


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PAGE DETAIL	IFPC HDMI/DP

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MISC: ROM, Straps

GROUP0 STRAP2	STRAP1	STRAP0	RAMCFG[4:0]	
L	L	L	00000	RAMCFG MICRON 8Gb G6X 19Gbps x16 (161-0430-900)
H	L	L	00100	RAMCFG MICRON 8Gb G6X 19Gbps x8 (161-0430-900)
H	L	H	00101	RAMCFG MICRON 8Gb G6X 21Gbps x8 (161-0431-900)
L	L	M	01000	RAMCFG MICRON 8Gb G6X 20Gbps x16 (161-0424-900)
L	L	H	00001	RAMCFG MICRON 8Gb G6X 21Gbps x16 (161-0431-900)
L	M	H	01010	RAMCFG MICRON 8Gb G6X 19Gbps x16 (161-0430-900)

ROM_SO	ROM_SI	ROM_SCLK	SMARTFAN[2:0],FS_OVERT	1:ENABLE 0:DISABLE	
H	H	H	0111	FS_OVERT ENABLE	DEFAULT
L	L	L	0000	FS_OVERT DISABLE	

STRAP5	STRAP4	STRAP3	SMB_ALT_ADDR	DEVID_SEL	PCIE_CFG	VGA_DEVICE
M	H	H	1	1	1	1
M	H	L	1	1	1	0
M	L	H	1	1	0	1
M	L	L	1	1	0	0
L	H	M	1	0	1	1
L	M	H	1	0	1	0
L	M	L	1	0	0	1
L	L	M	1	0	0	0
H	H	H	0	1	1	1
H	H	L	0	1	1	0
H	L	H	0	1	0	1
H	L	L	0	1	0	0
L	H	H	0	0	1	1
L	H	L	0	0	1	0
L	L	H	0	0	0	1
L	L	L	0	0	0	0

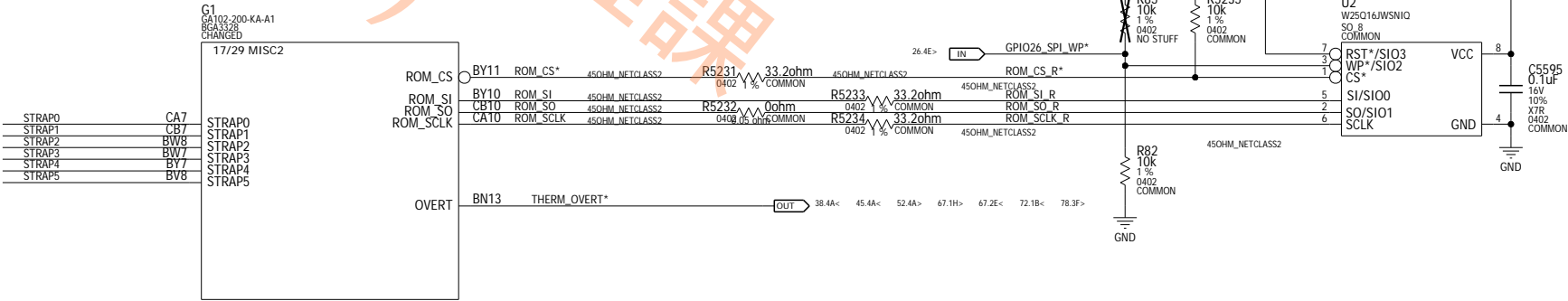
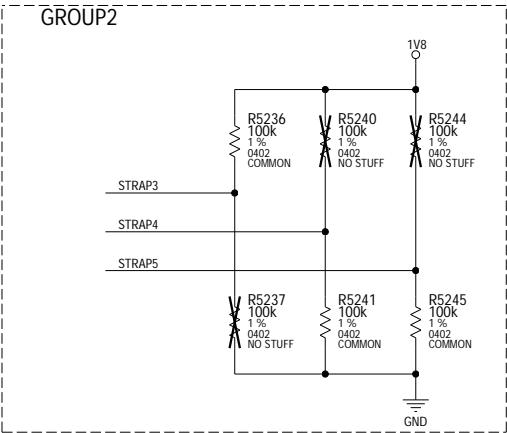
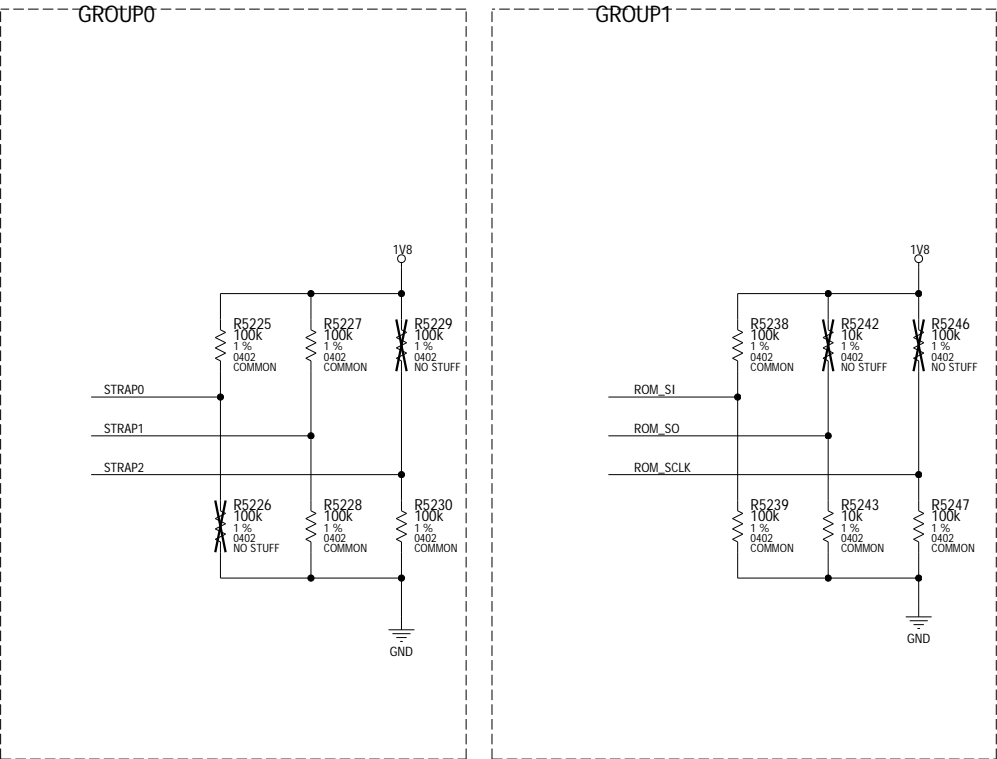
Default

H=High :Tied to 1.8V  
M=Middle:Tied to 0.9V  
L=Low :Tied to 0V

<-A01 PCB

<-OLD

<-LATEST A02 PCB



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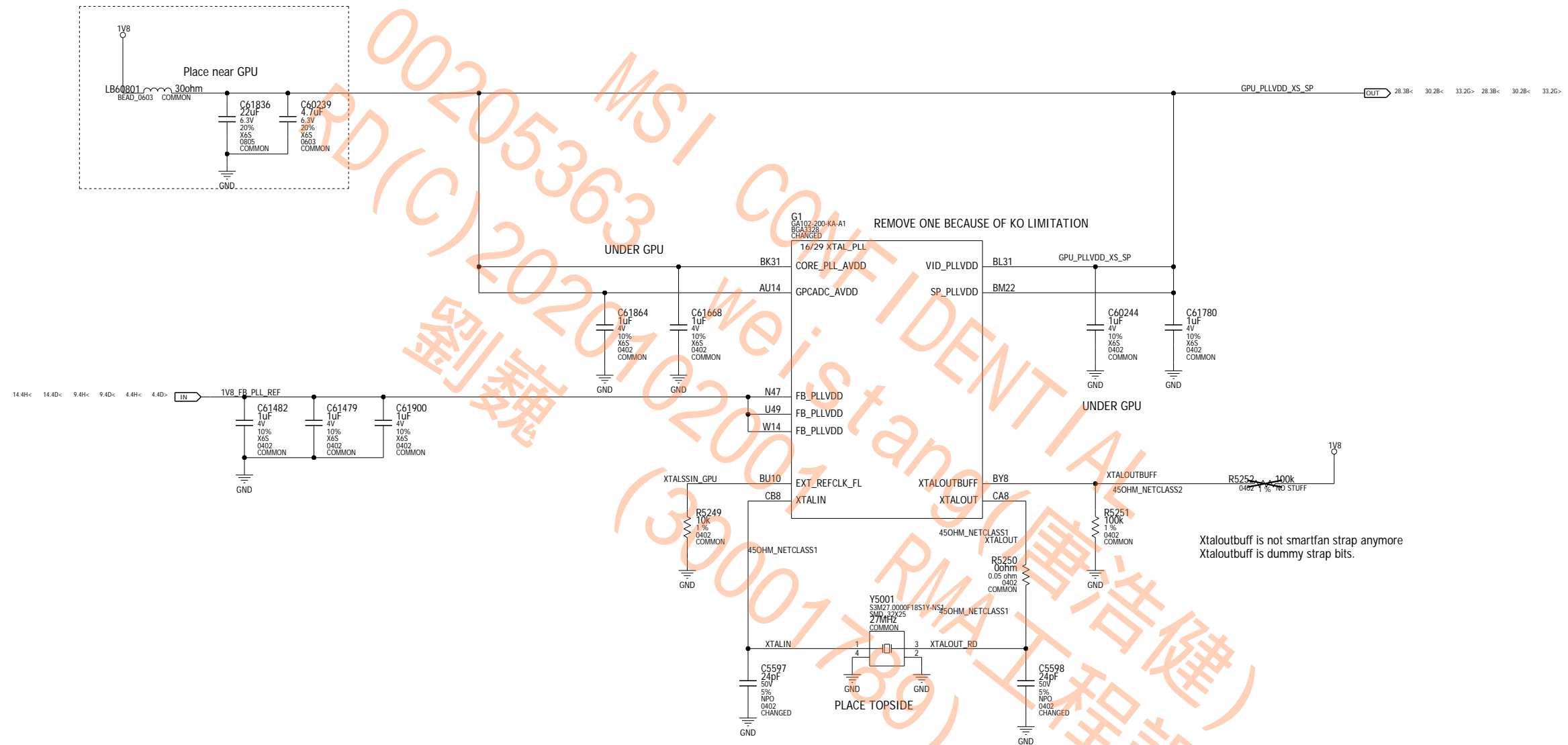
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## MISC: XTAL. PLL



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PS: FBVDD Controller OVR3

PIN12 MP2988 VIN SENSE 1/16 DIVIDER  
PIN12 UP9529 REFOUT

1

2

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1

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FOR DT SKU

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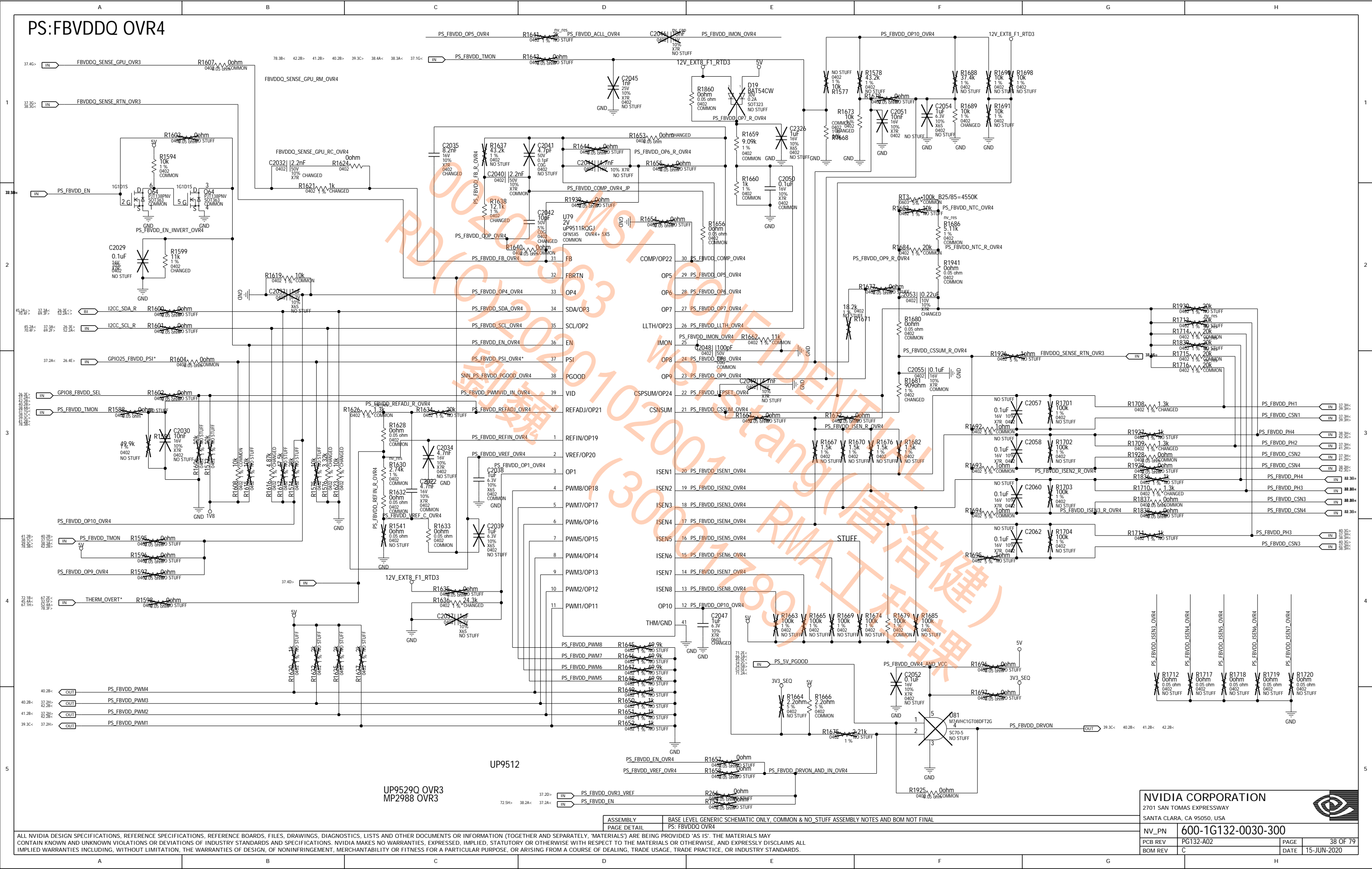
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ASSEMBLY PAGE DETAIL BASE LEVEL GENERIC SCHEMATIC ONLY, COMMON & NO\_STUFF ASSEMBLY NOTES AND BOM NOT FINAL PS: FBVDD Controller OVR3

PS:FBVDDQ OVR4



UP9529Q OVR3  
MP2988 OVR3

UP9512

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PAGE DETAIL	PS: FBVDDQ OVR4

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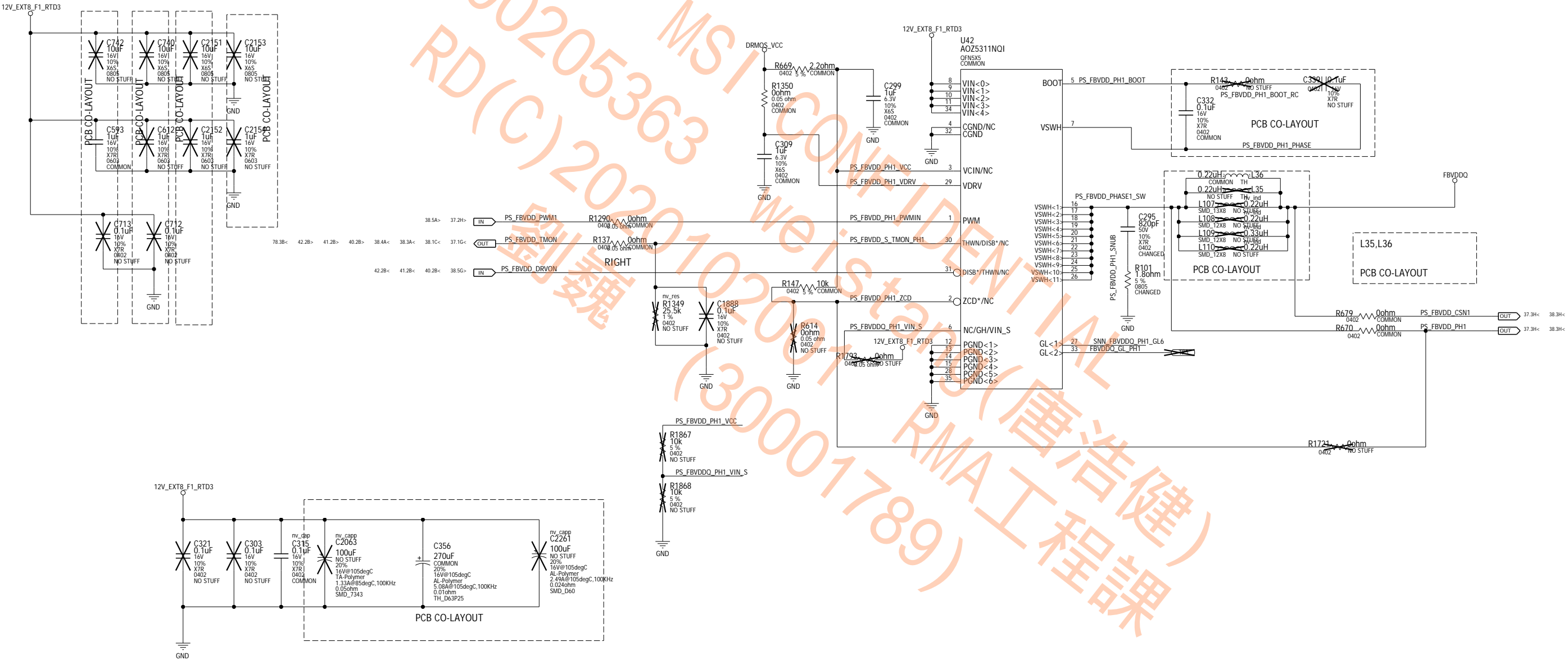
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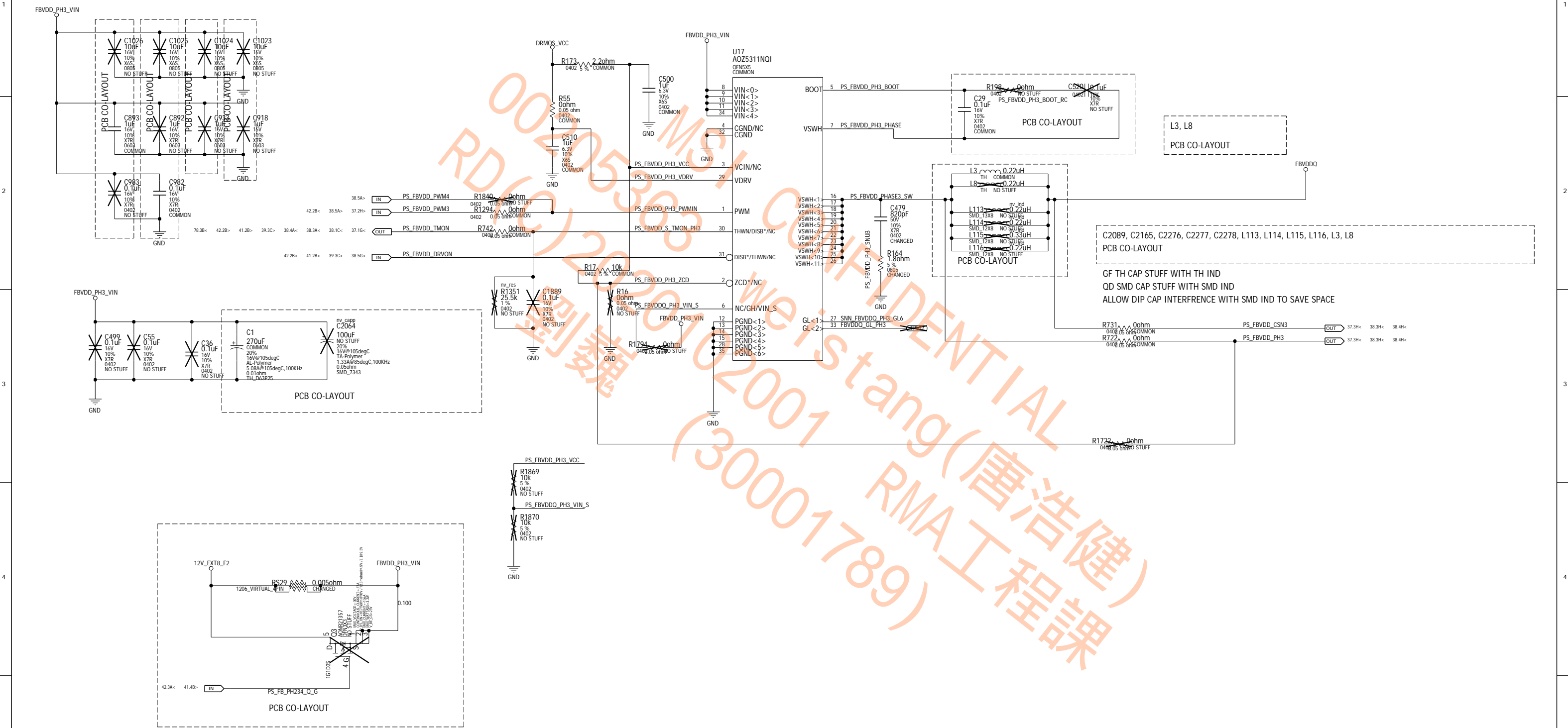
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PS: FBVDD PH1



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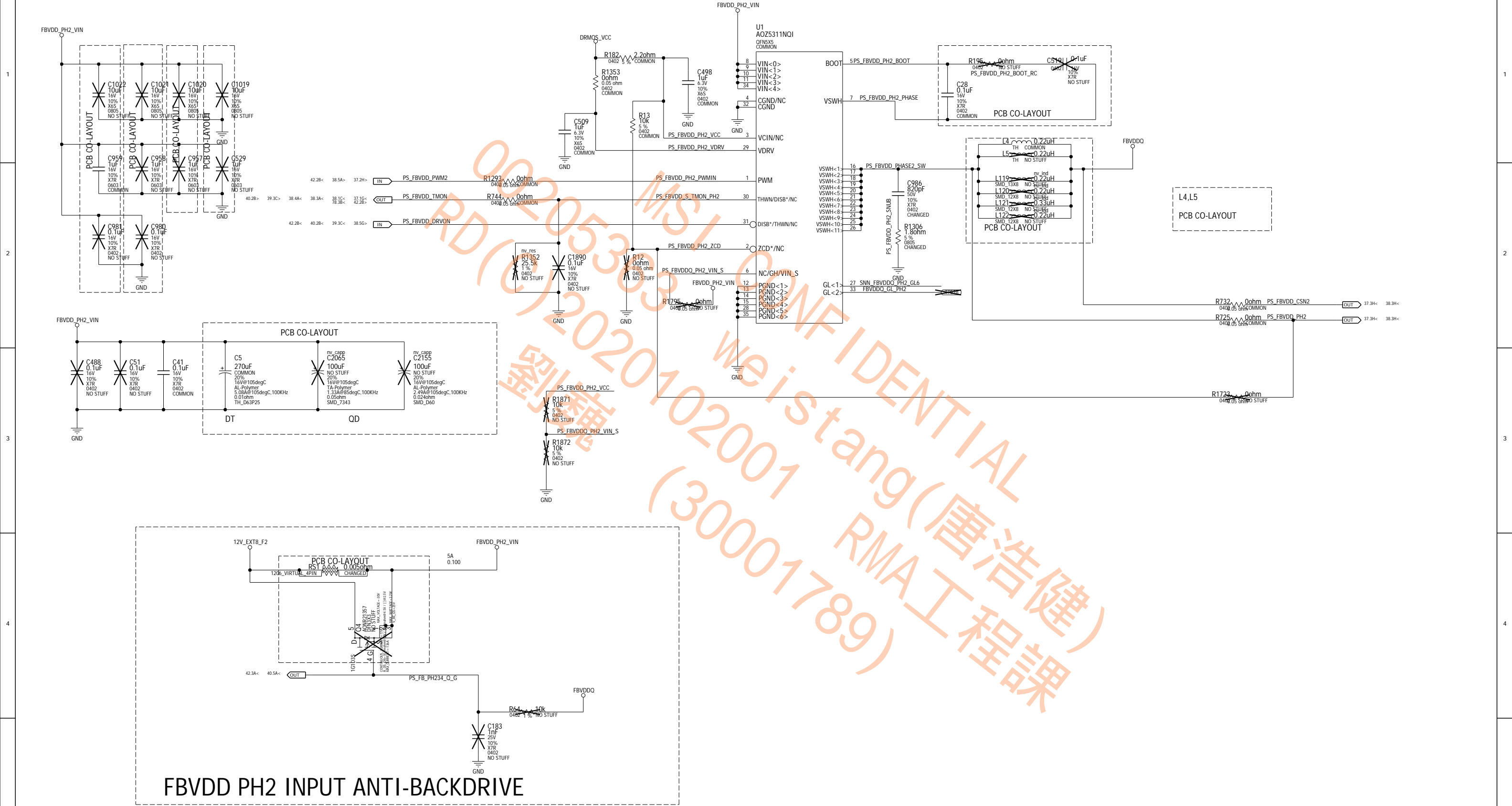
PS: FBVDD PH3



FBVDD PH3 INPUT ANTI-BACKDRIVE

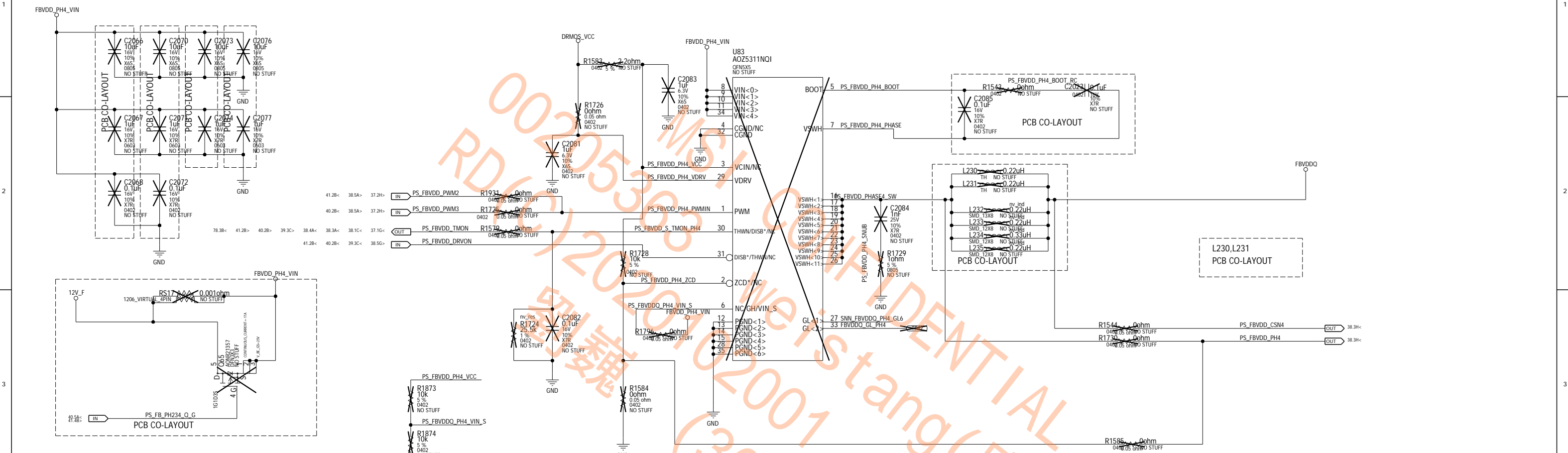
ALL NVIDIA DESIGN SPECIFICATIONS, REFERENCE SPECIFICATIONS, REFERENCE BOARDS, FILES, DRAWINGS, DIAGNOSTICS, LISTS AND OTHER DOCUMENTS OR INFORMATION (TOGETHER AND SEPARATELY, "MATERIALS") ARE BEING PROVIDED "AS IS". THE MATERIALS MAY CONTAIN KNOWN AND UNKNOWN VIOLATIONS OR DEVIATIONS OF INDUSTRY STANDARDS AND SPECIFICATIONS. NVIDIA MAKES NO WARRANTIES, EXPRESSED, IMPLIED, STATUTORY OR OTHERWISE WITH RESPECT TO THE MATERIALS OR OTHERWISE, AND EXPRESSLY DISCLAIMS ALL IMPLIED WARRANTIES INCLUDING, WITHOUT LIMITATION, THE WARRANTIES OF DESIGN, OF NON-INFRINGEMENT, MERCHANTABILITY OR FITNESS FOR A PARTICULAR PURPOSE, OR ARISING FROM A COURSE OF DEALING, TRADE USAGE, TRADE PRACTICE, OR INDUSTRY STANDARDS.

PS: FBVDD PH2

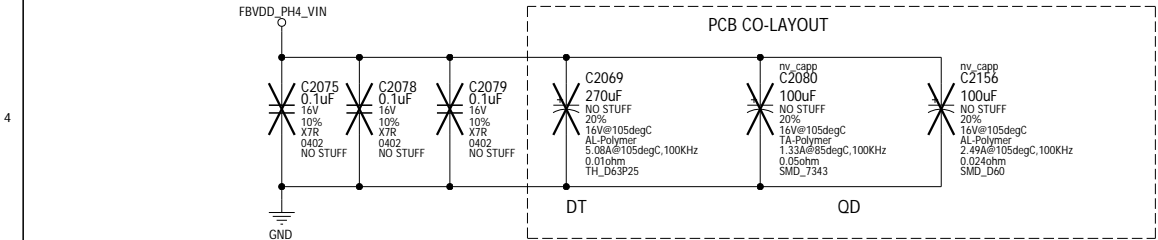


FBVDD PH2 INPUT ANTI-BACKDRIVE

PS: FBVDD PH4



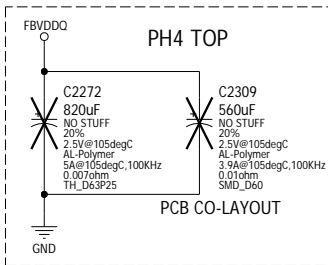
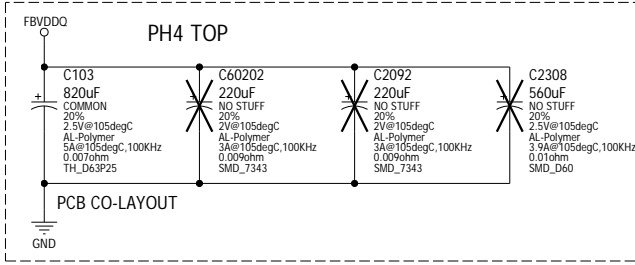
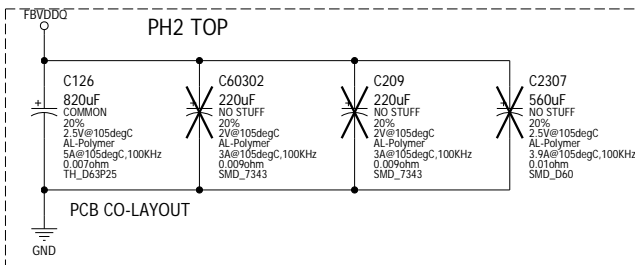
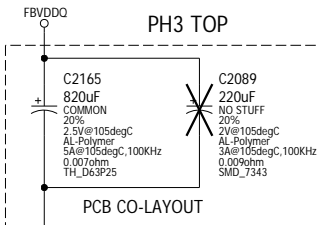
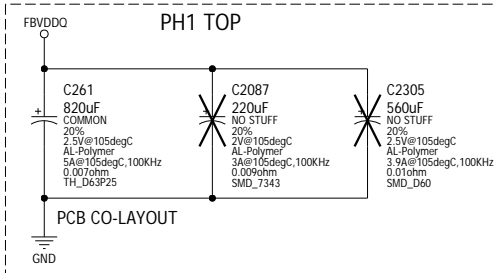
FBVDD PH3 INPUT ANTI-BACKDRIVE



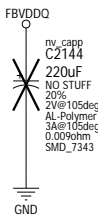
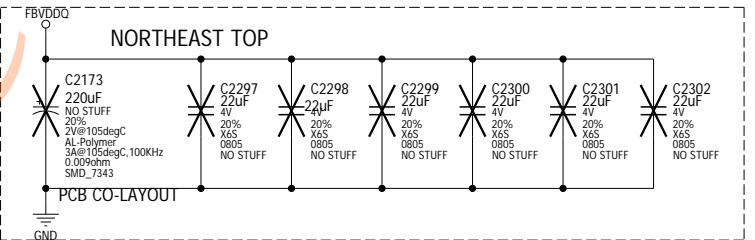
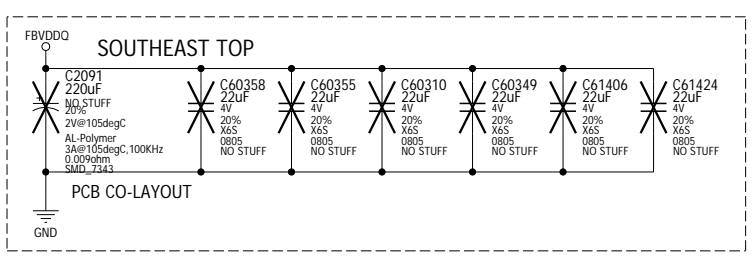
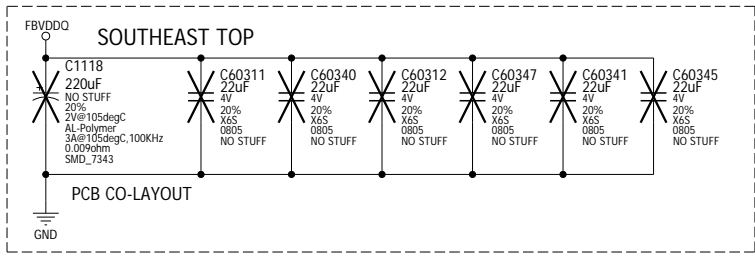
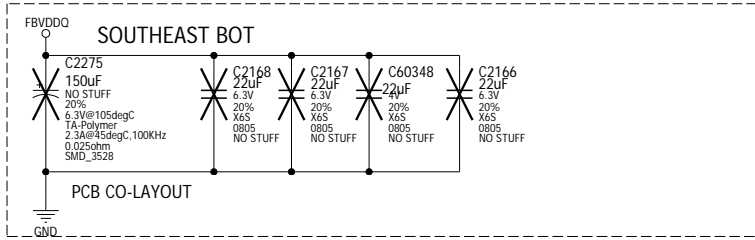
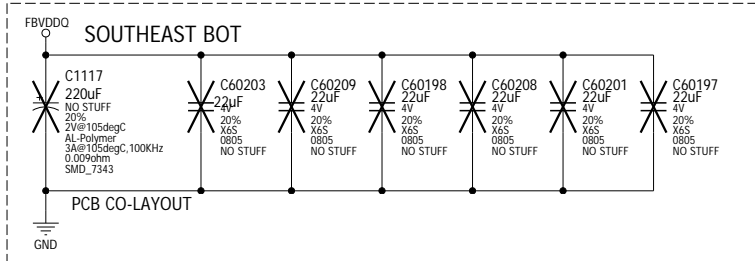
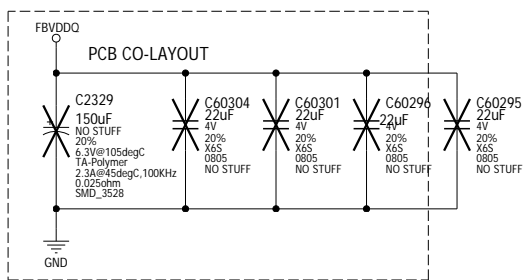
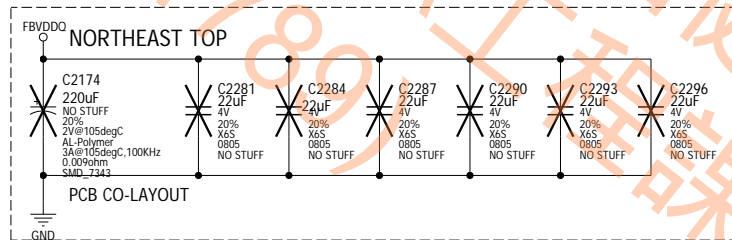
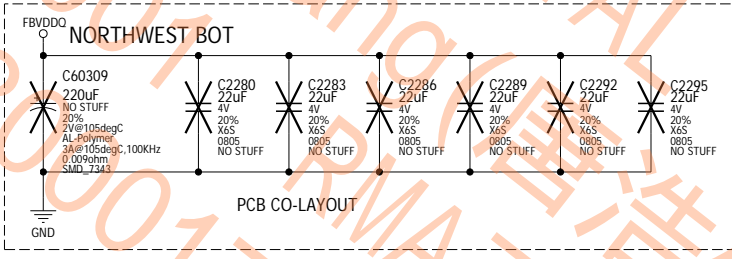
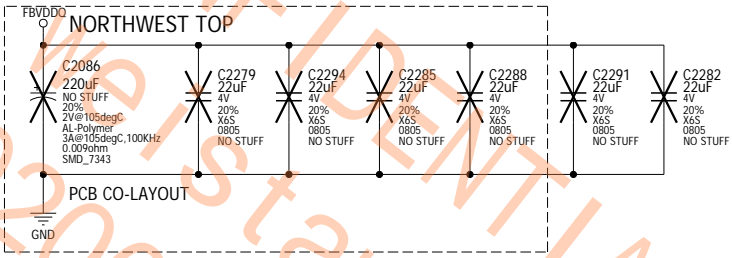
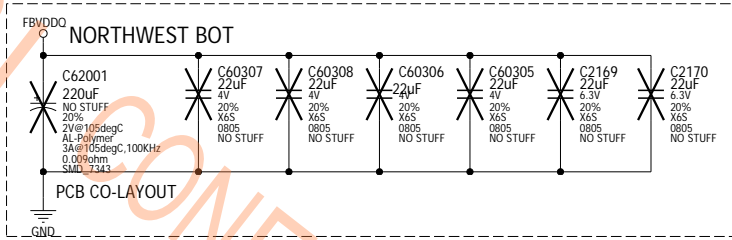
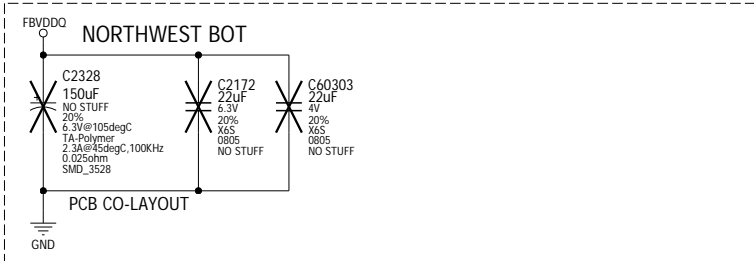
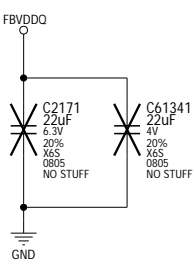
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PS: FBVDD OUTPUT CAP

3x 820uF TH FBVDD OUTPUT BULK CAPS



CLOSE TO FB PH3 OUTPUT STUFF



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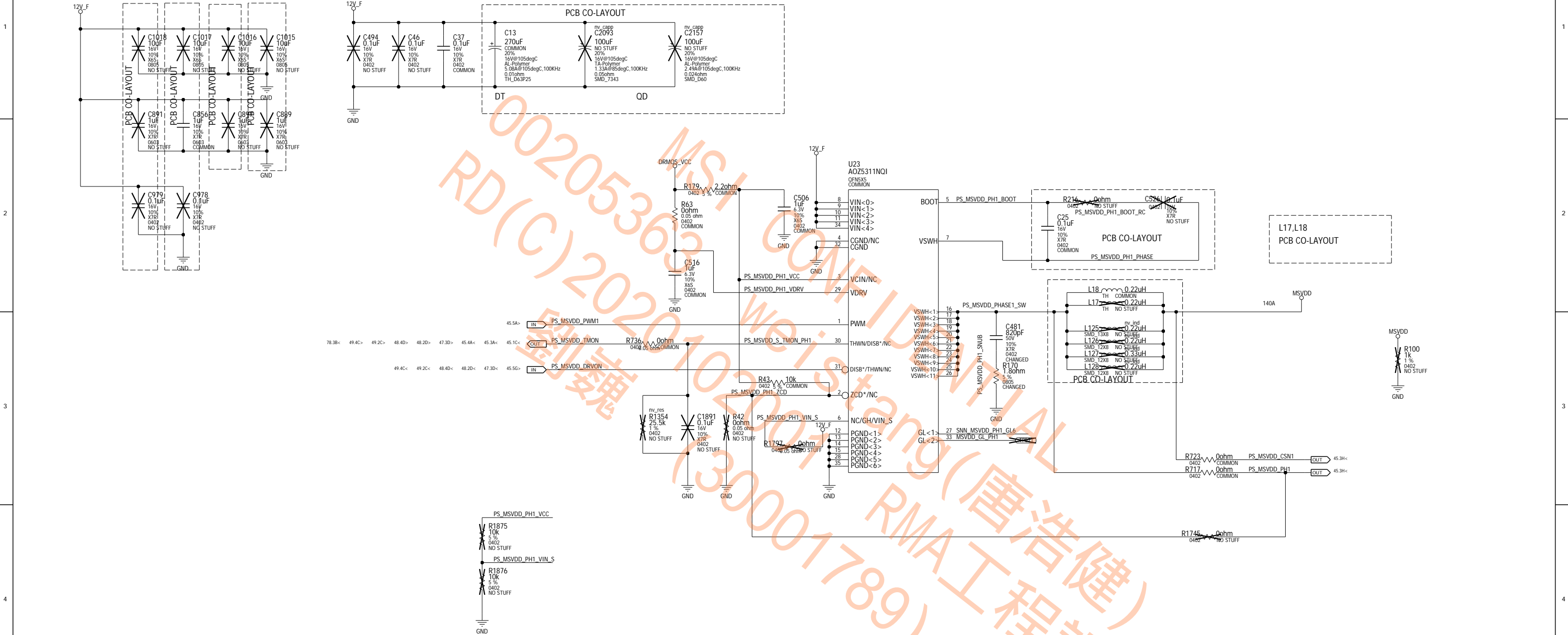
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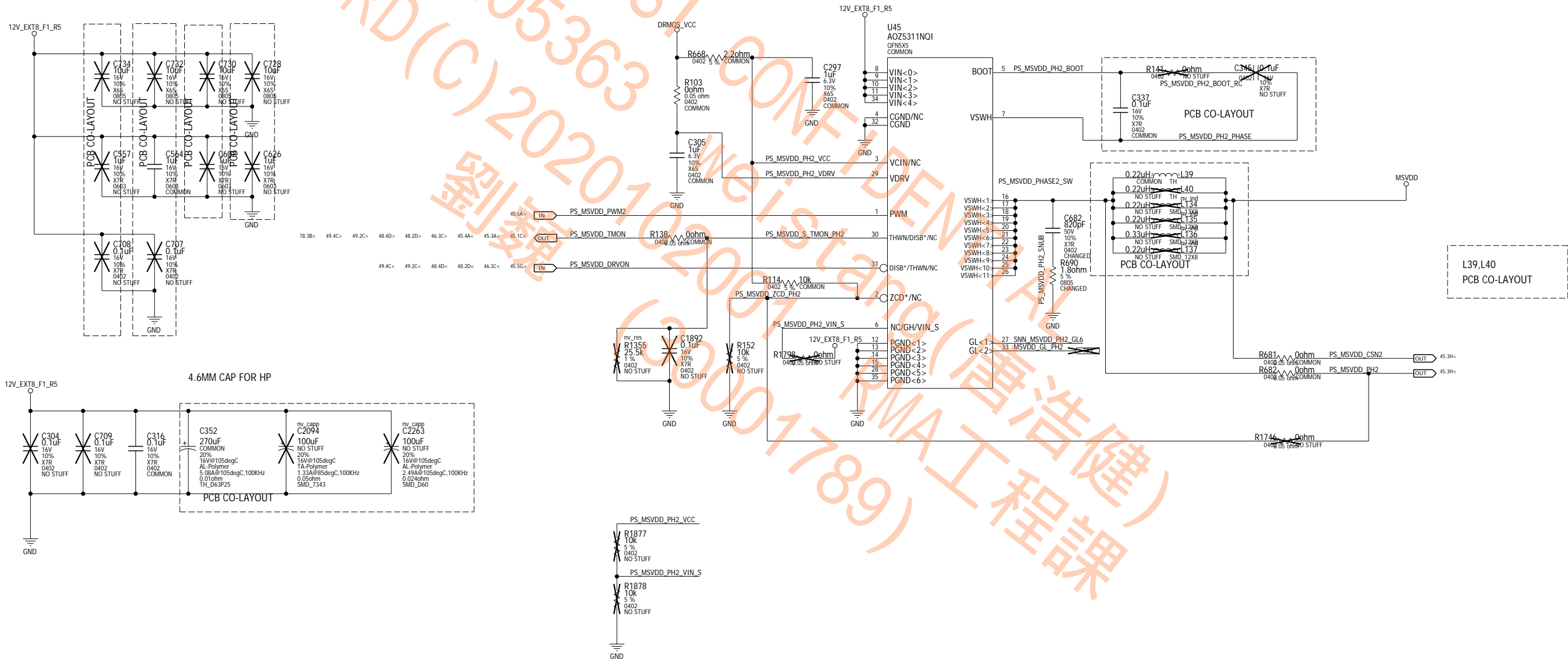


PS: MSVDD Phase 1



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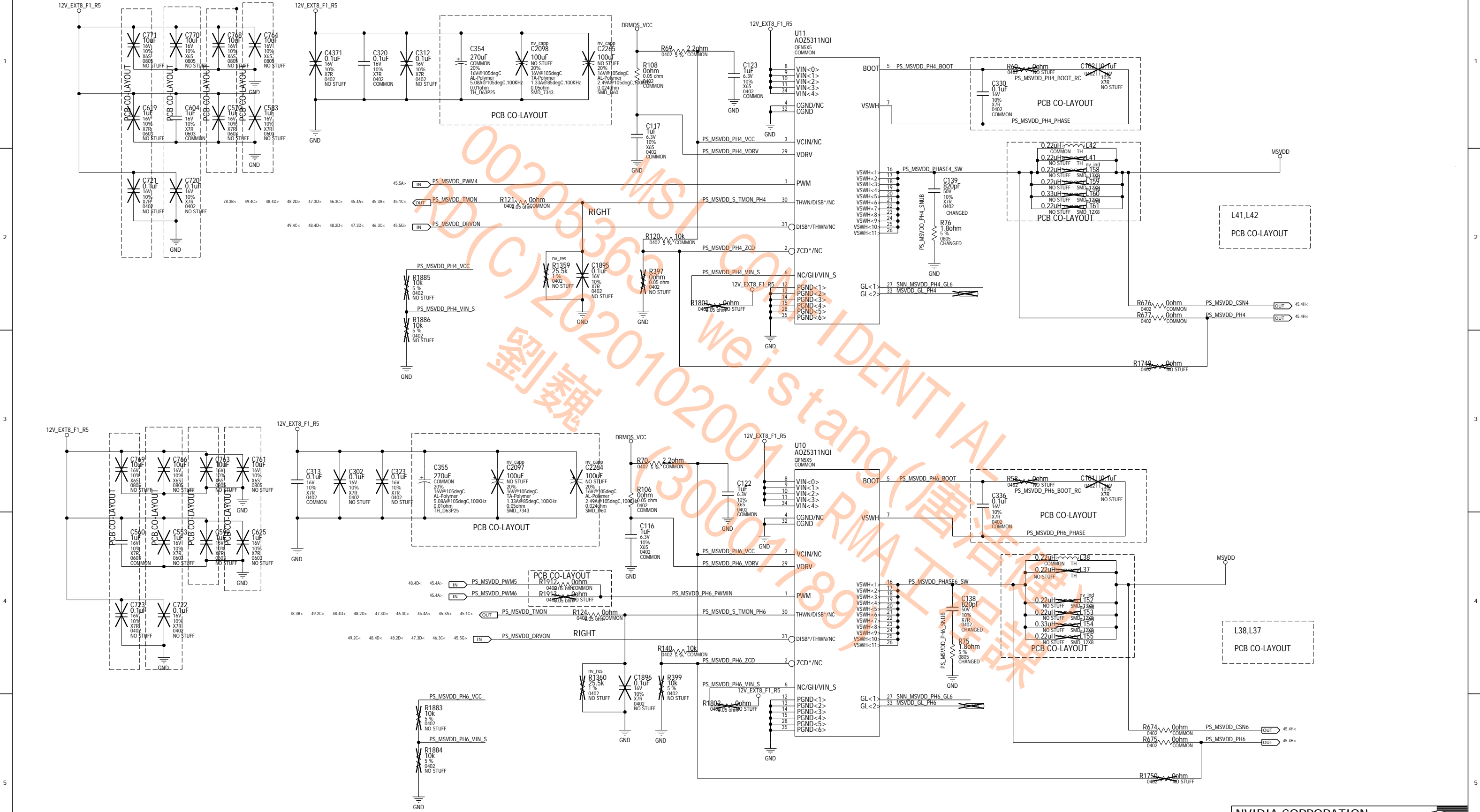
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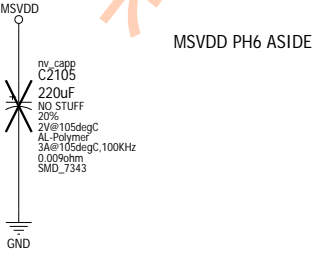
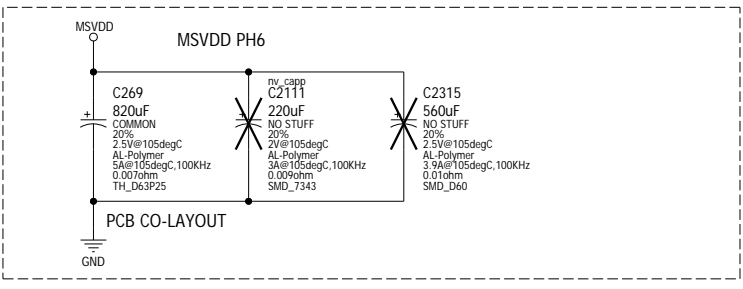
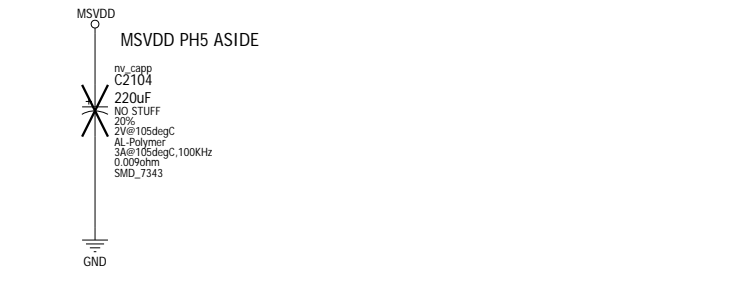
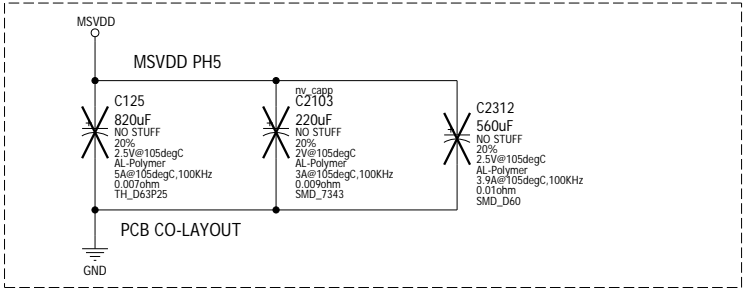
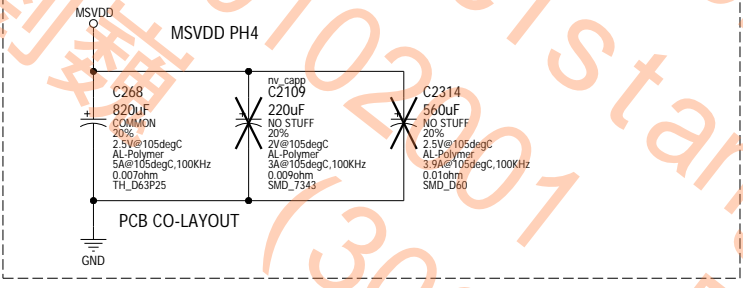
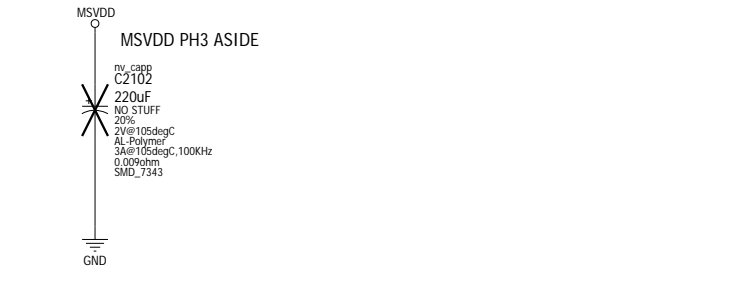
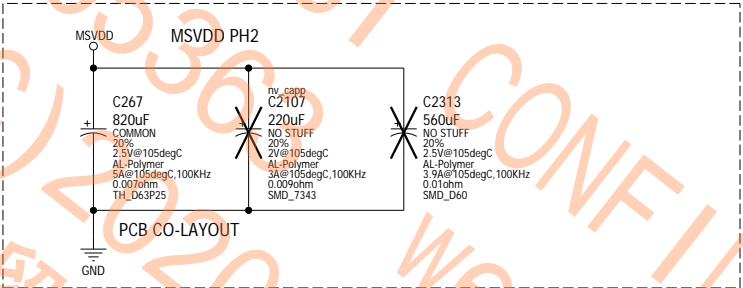
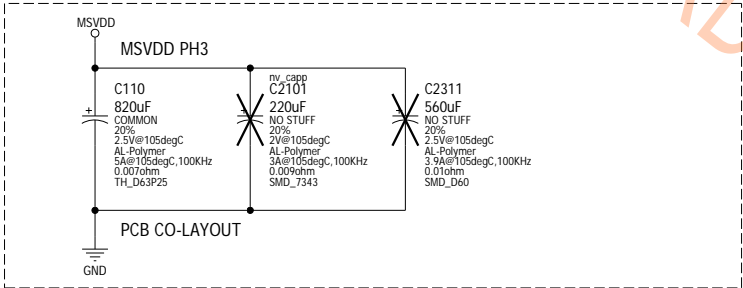
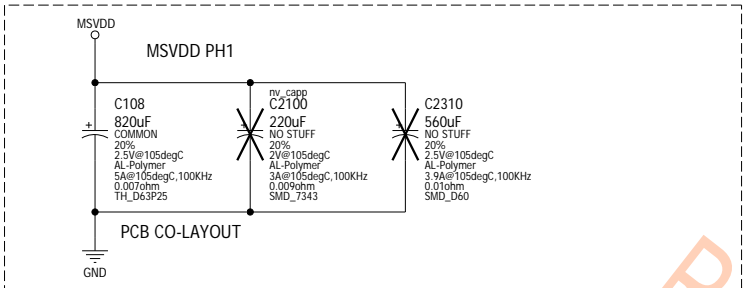


PS: MSVDD PH 4,6



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PAGE DETAIL PS: MSVDD OUTPUT CAP(TOP)

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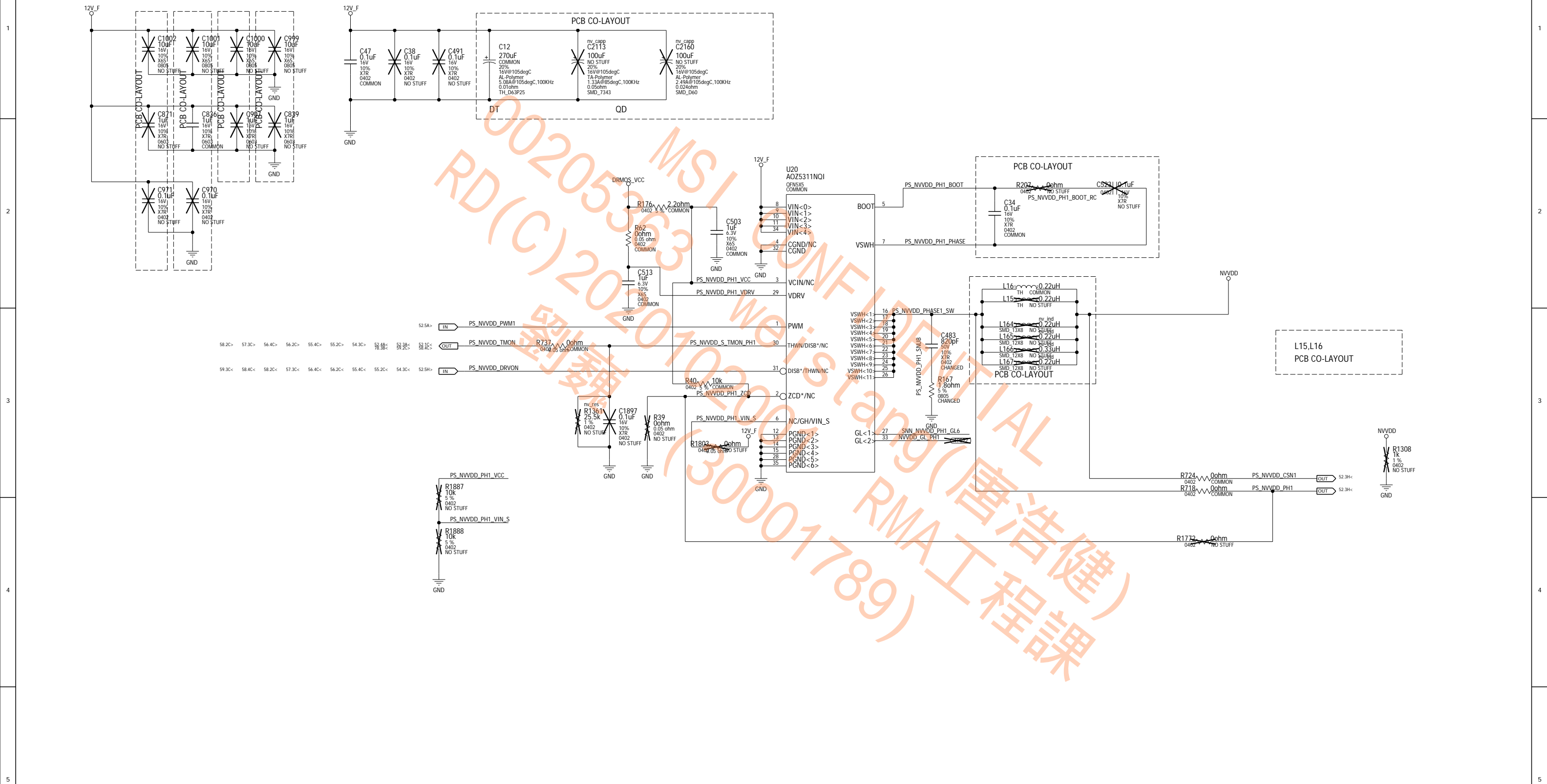
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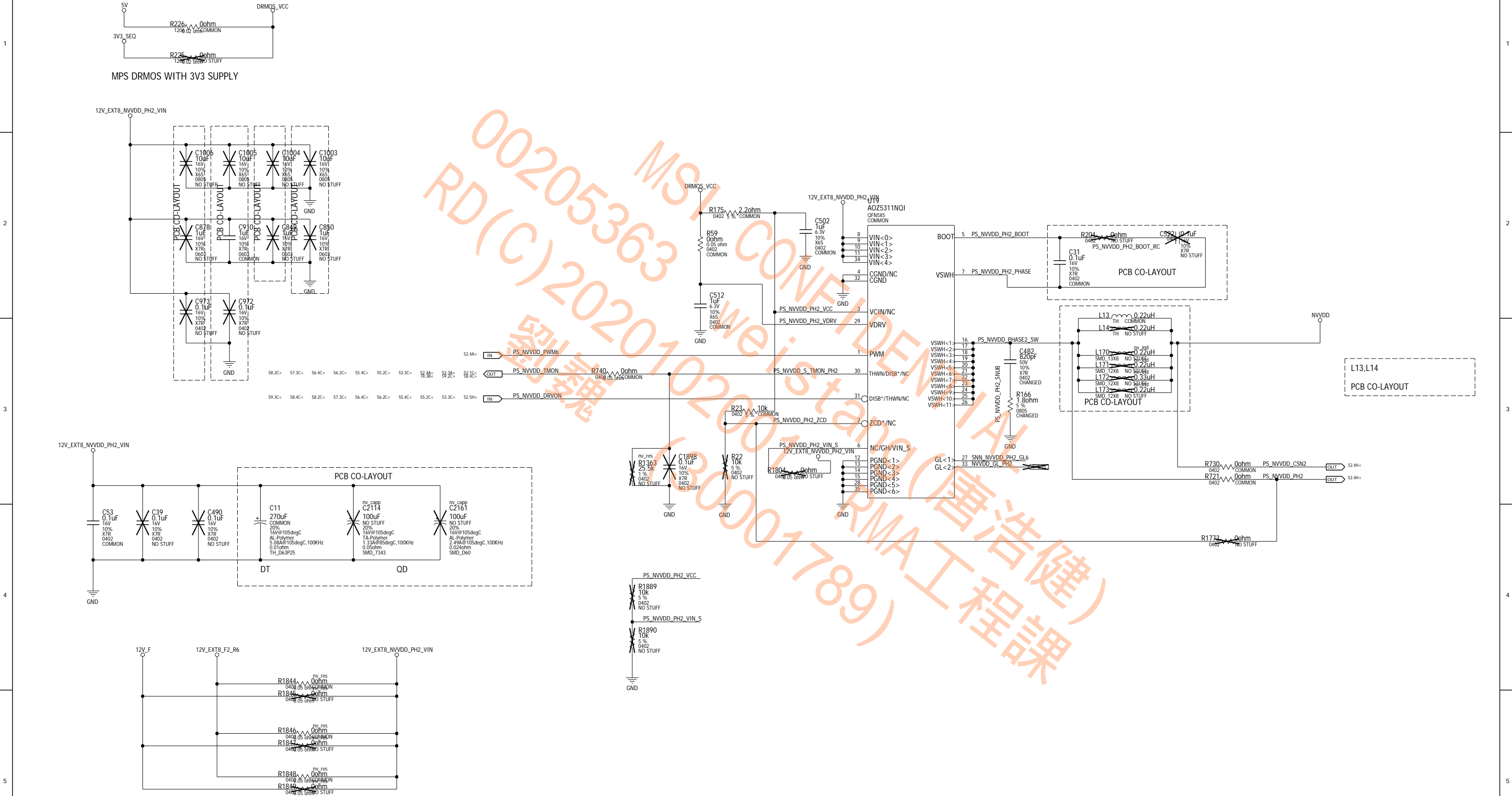


PS: NVVDD Phase 1



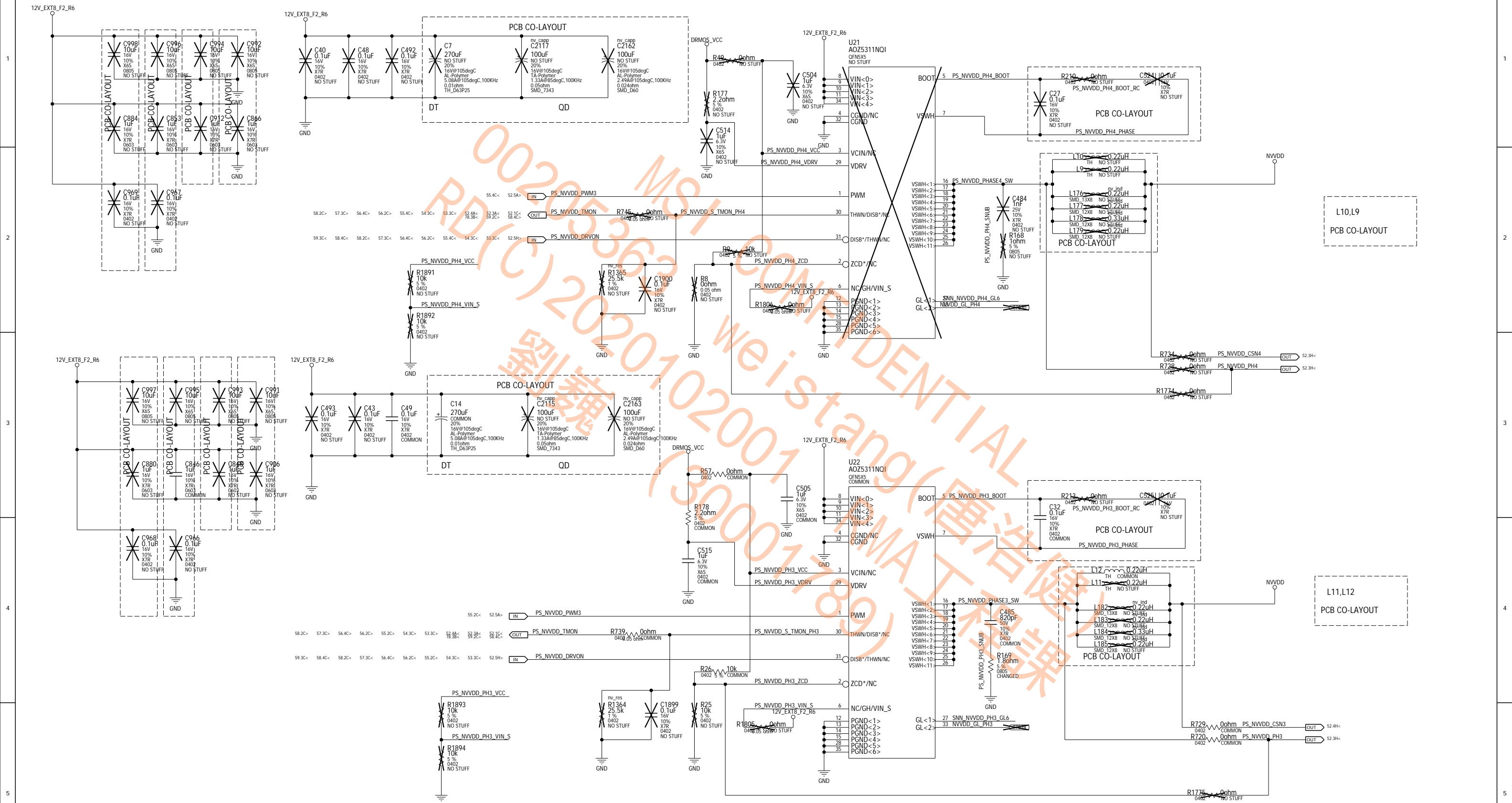
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PS: NVVDD Phase 2



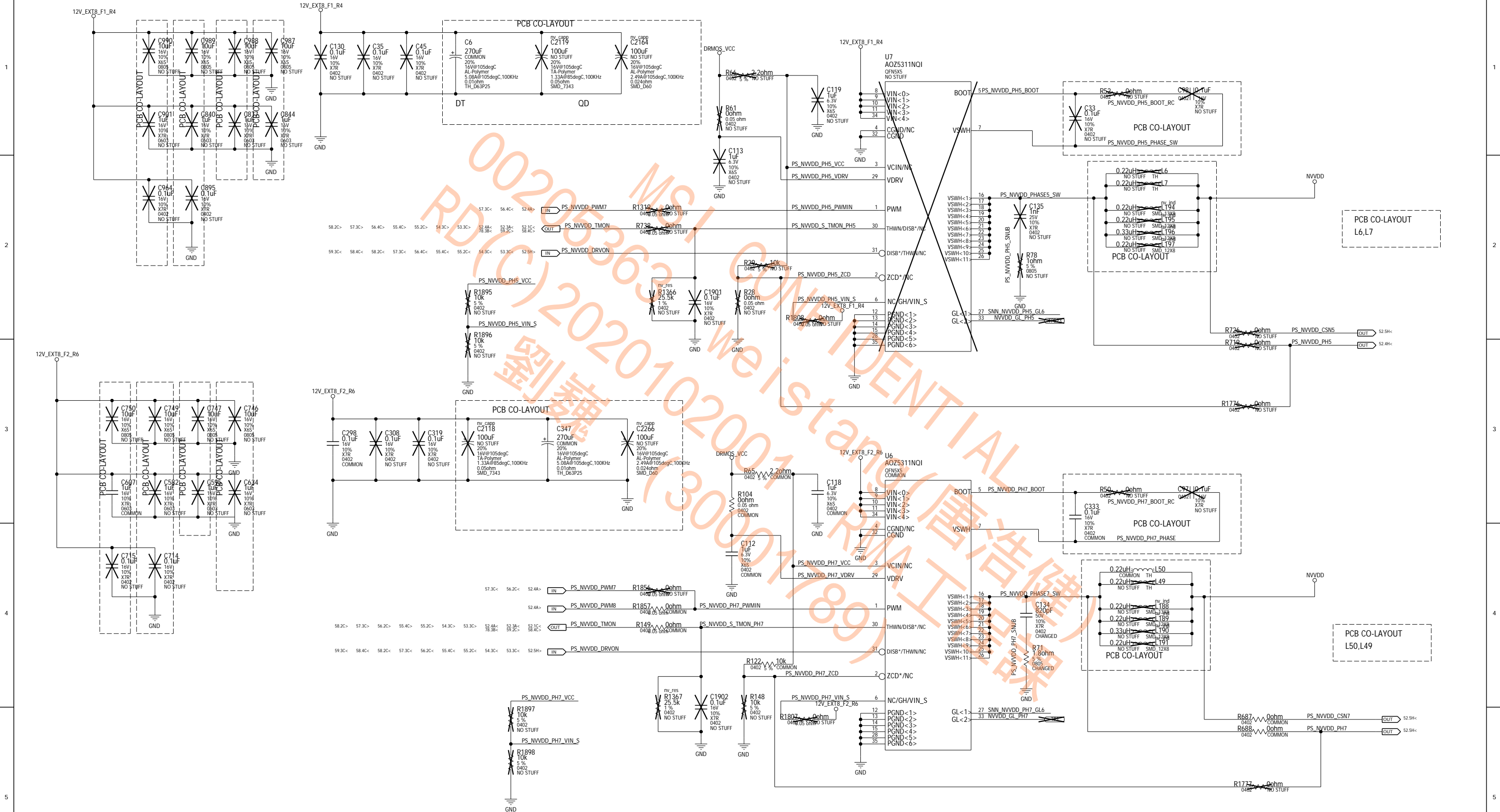
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# PS: NVVDD Phase 3 (PWM6), PHASE 4(PWM2)



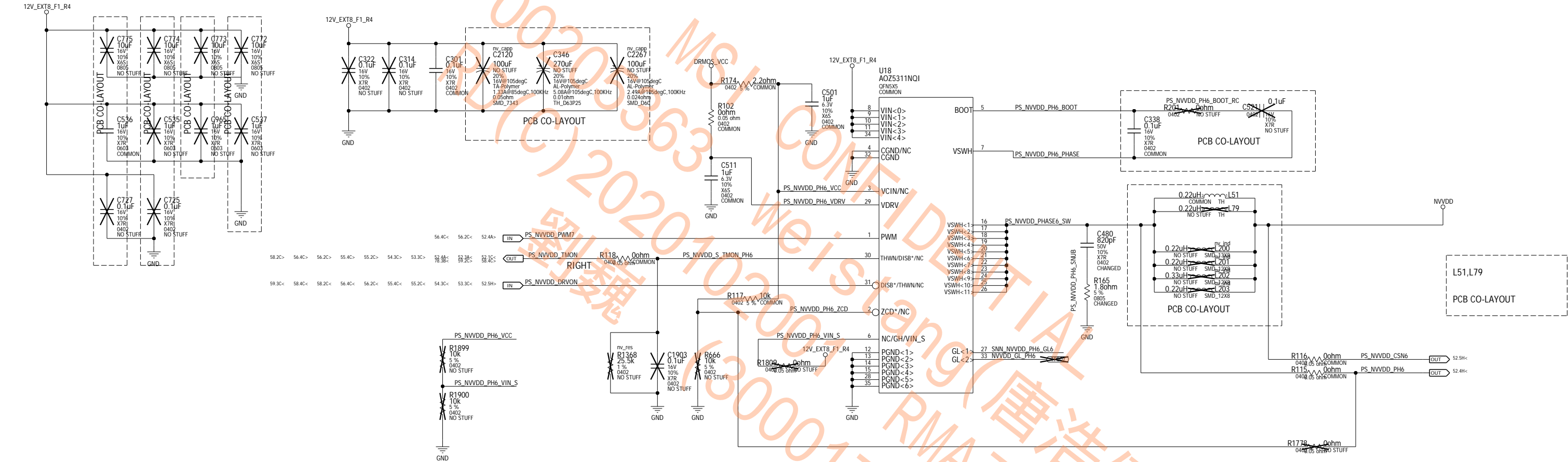
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PS: NVVDD PH 5(PWM2), PH7(PWM3)



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PS: NVVDD Phase 6(PWM7)



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PCB CO-LAYOUT  
C131, C132, C133, C140, C141, C142, C2106, C2107, C2133, C2134, C2135, C2137, C264, C265, C266, C267, C61, C64, C65, C66, C67, C68, C69, C77, C80, C83, C88

PCB CO-LAYOUT  
C103, C105, C106, C107, C108, C110, C1208, C125, C1254, C1256, C1267, C128, C144, C145, C146, C151, C152, C153, C154, C159, C160, C161, C162, C170, C173, C2092, C2100, C2101, C2102, C2103, C2104, C2138, C2139, C2140, C2141, C2142, C2143, C2144, C2272, C230, C231, C232, C233, C243, C245, C246, C248, C253, C254, C255, C256, C270, C274, C275, C276, C283, C289, C290, C291, C60202, C62004, C62005

C163, C164, C166, C167, C2087, C2105, C2109, C2110, C2111, C224, C225, C261, C268, C269, C60354, C61428, C62, C63, C89, C91, C96

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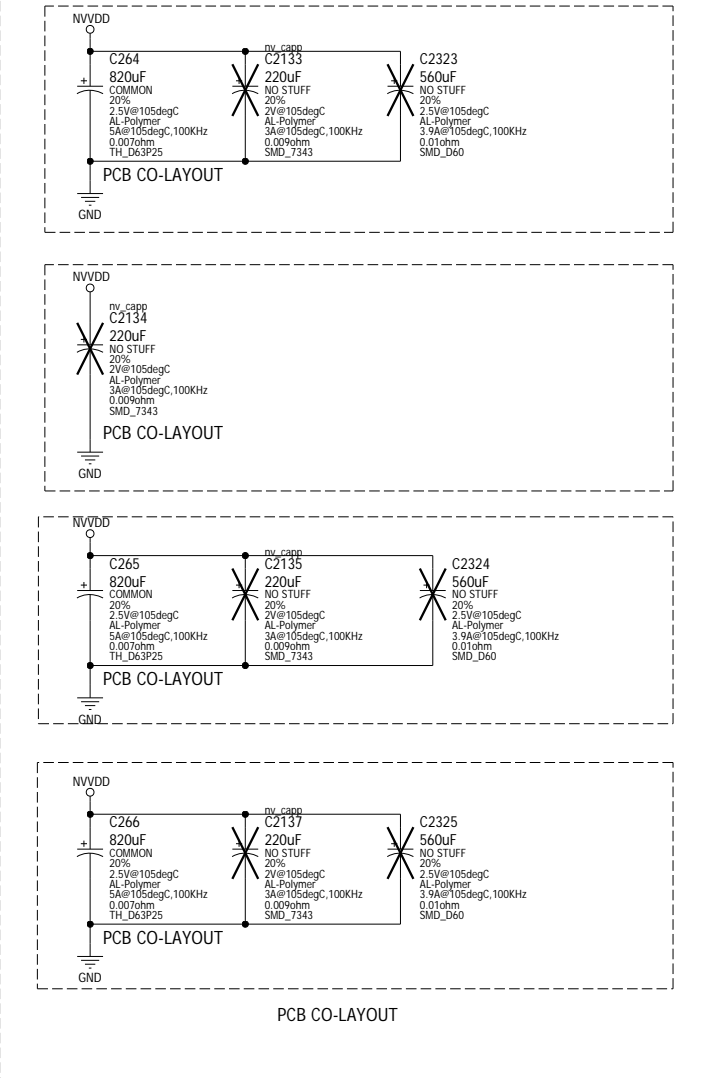
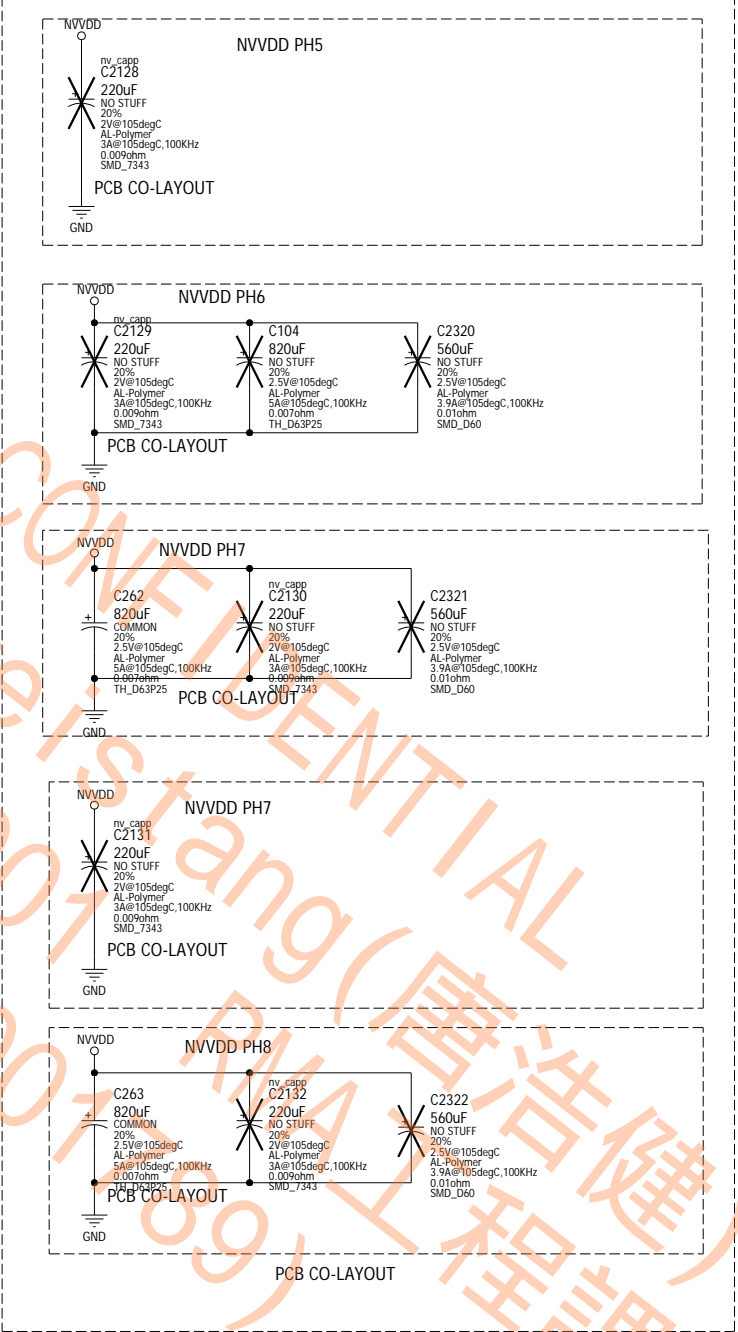
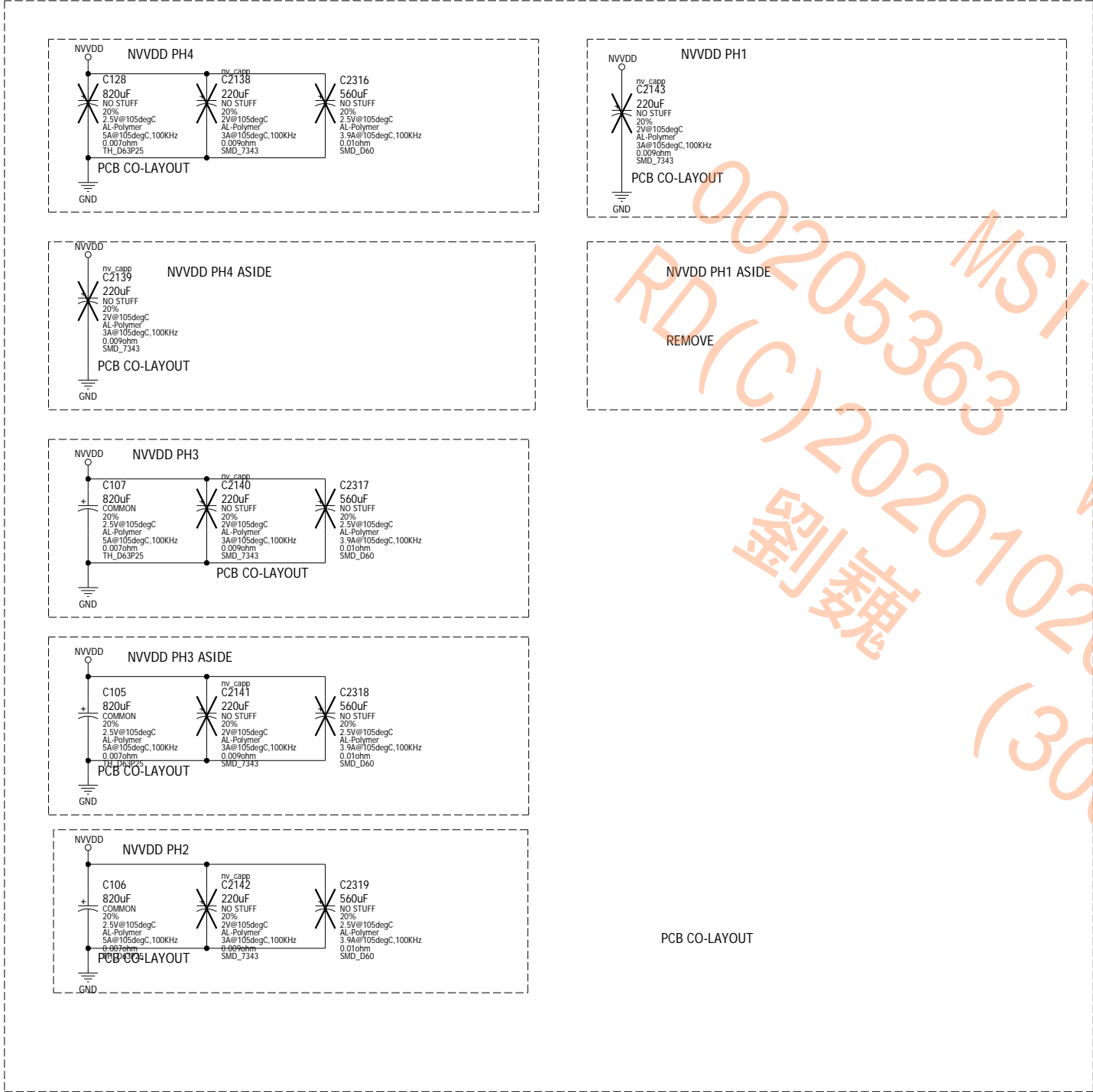
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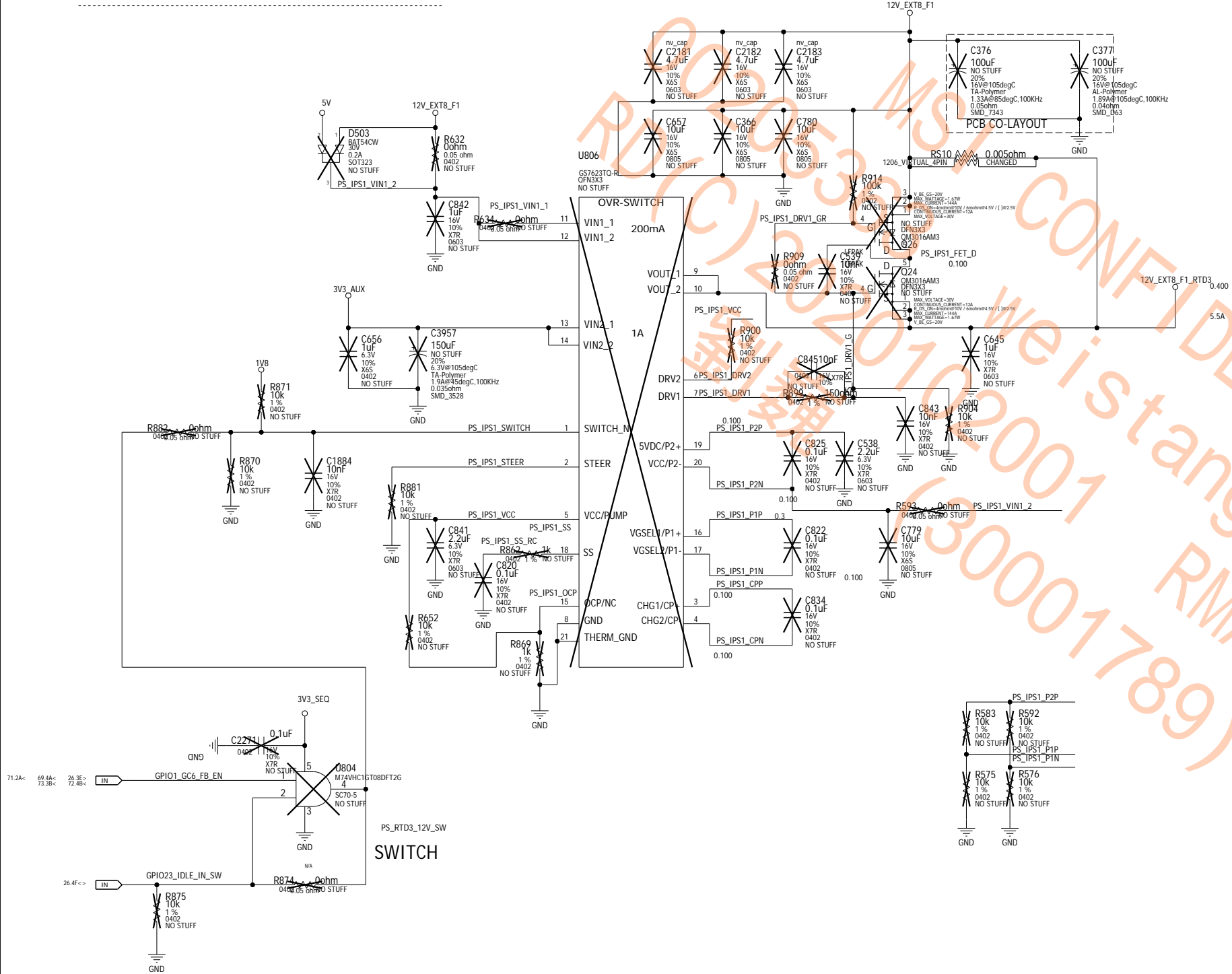


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PS: INPUT SWITCH RTD3

AND GATE LOGIC FOR P-BOARD

GPIO1	GPIO23	SWITCH	VOUT
0	0	0	12V_F
0	1	0	12V_F
1	0	0	12V_F
1	1	1	3V3A



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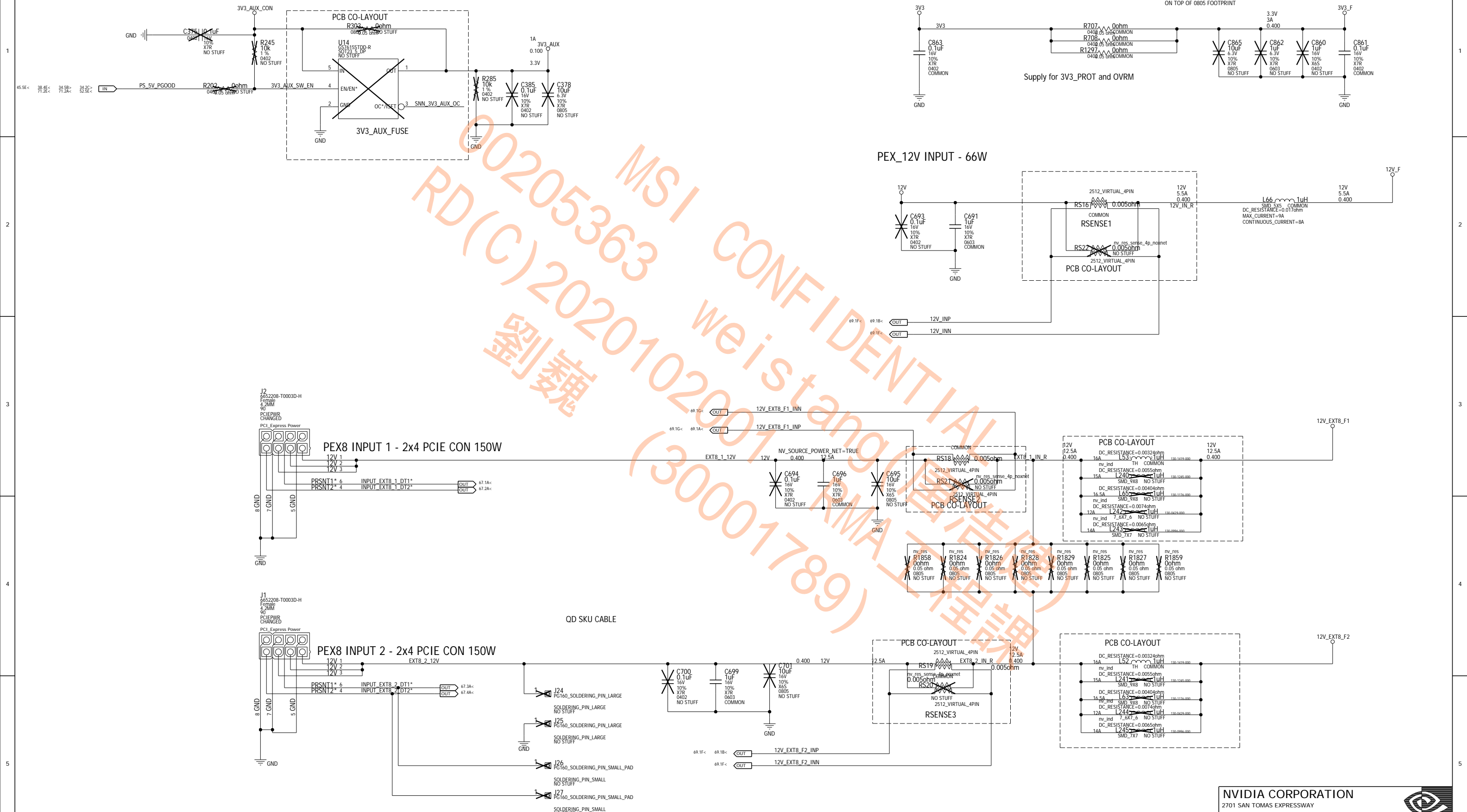
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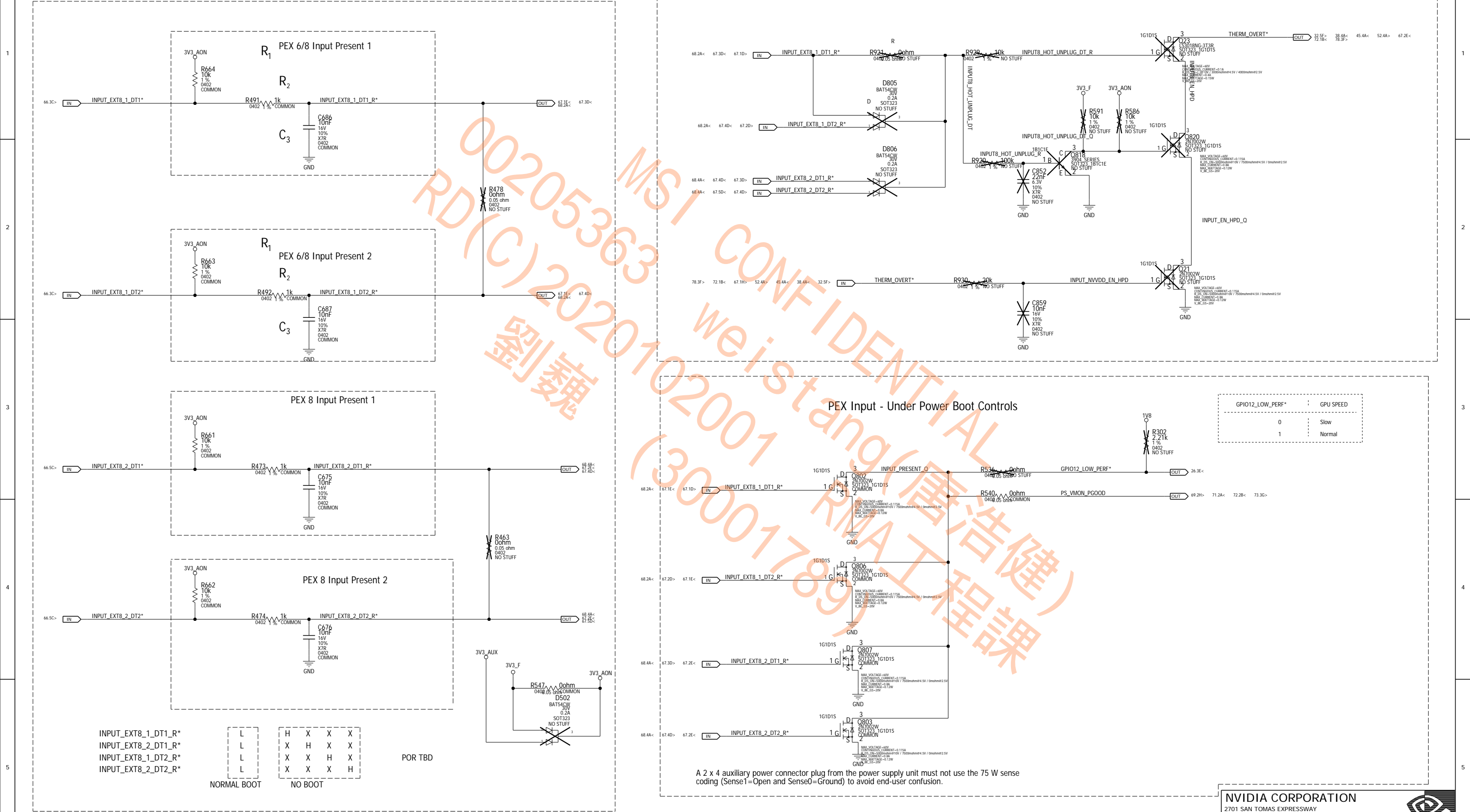
NV_PN	600-1G132-0030-300		
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PS: Inputs, Filtering, and Monitoring



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PS: 12V Current Steering & Hot Unplug Detect



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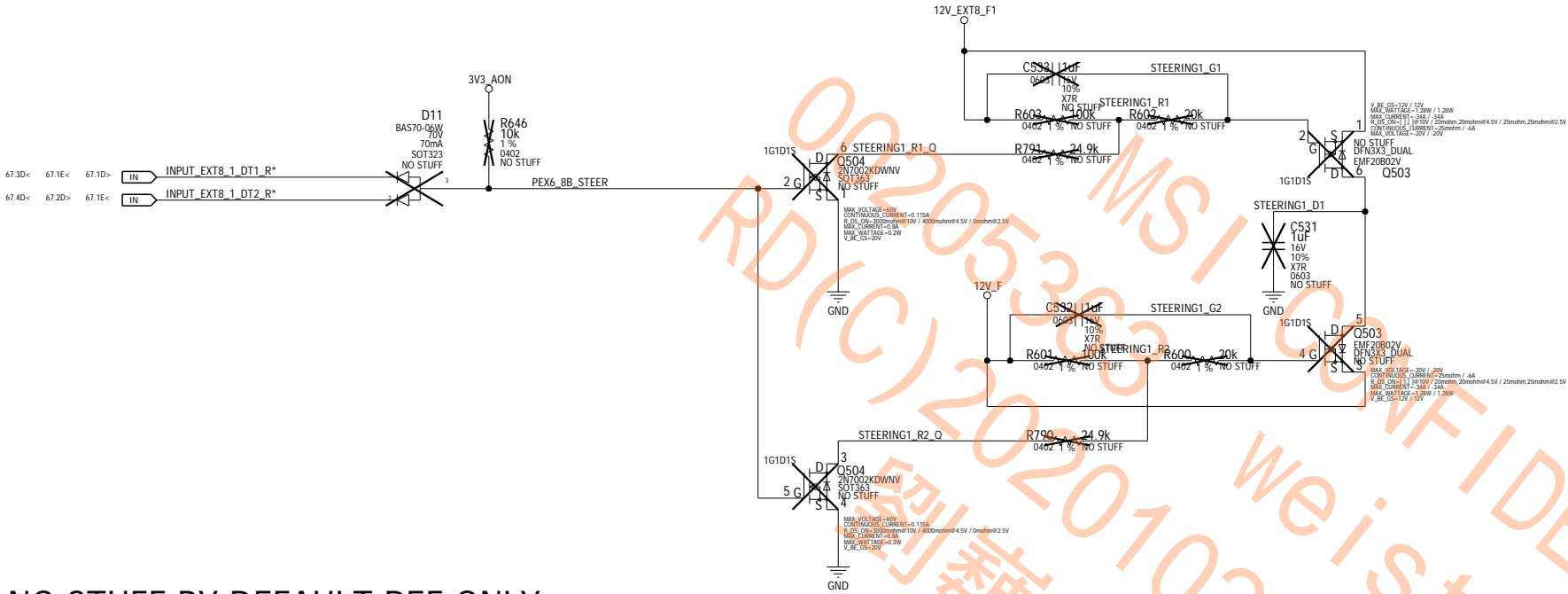
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PS: Discrete Steering

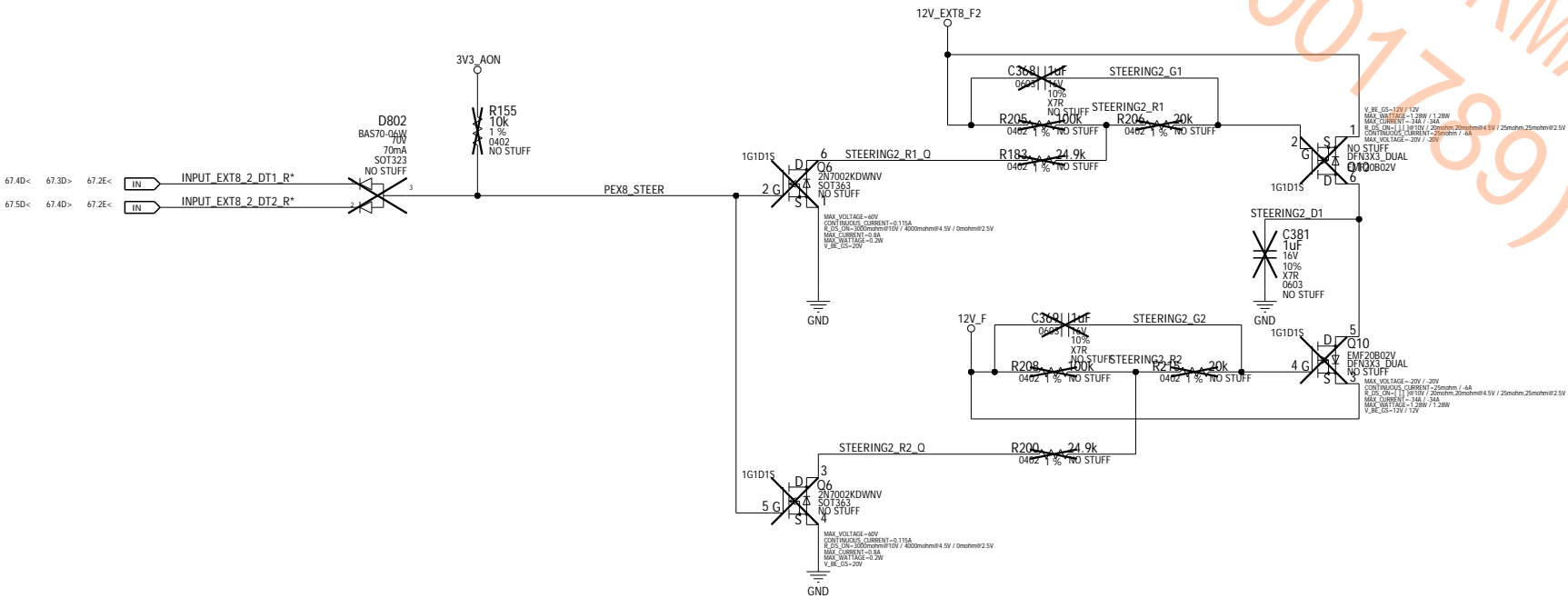
12V CURRENT STEERING (UNDER POWER BOOT):

PEX12V AND 12V\_EXT8\_F1



NO STUFF BY DEFAULT REF ONLY

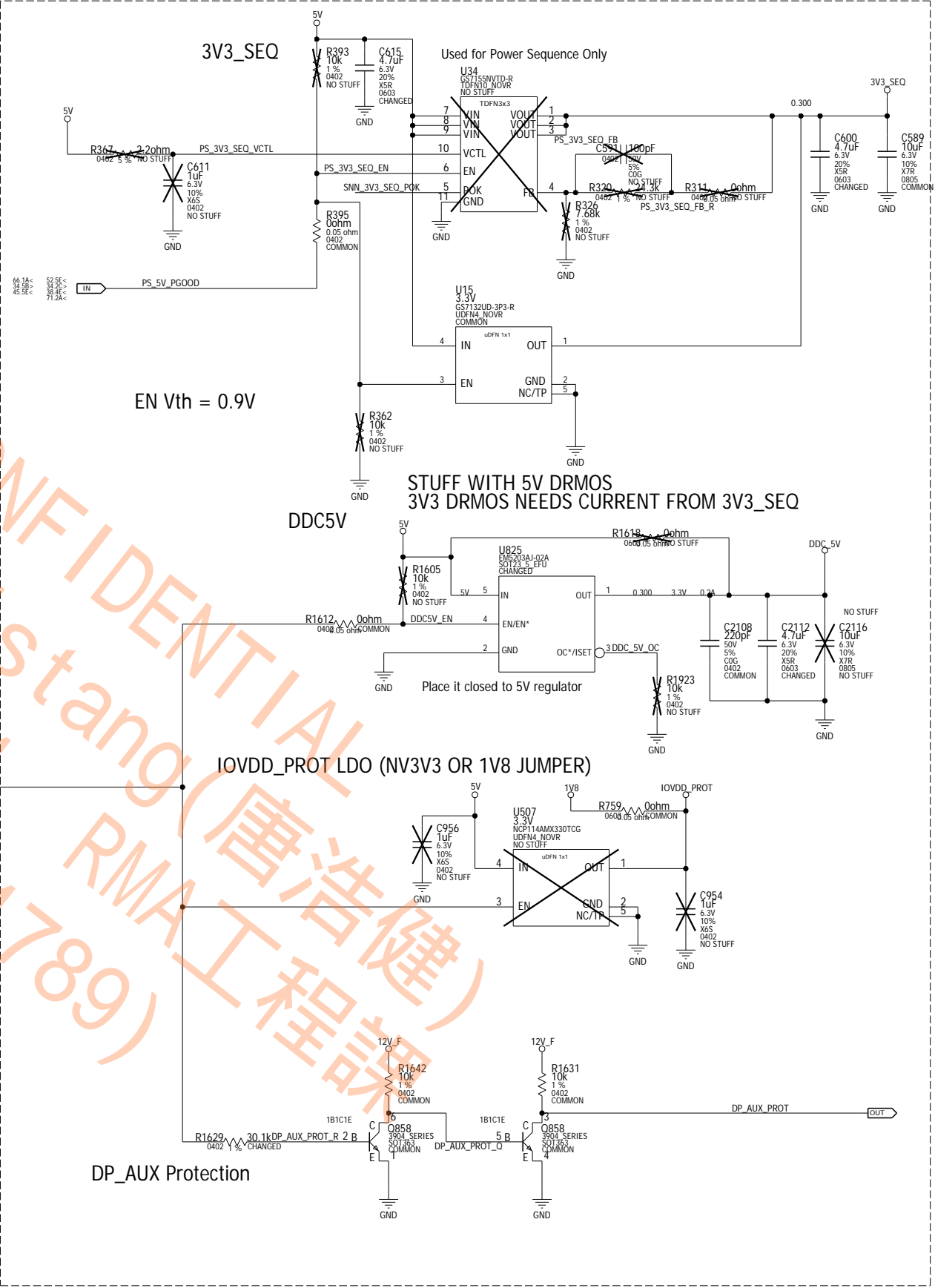
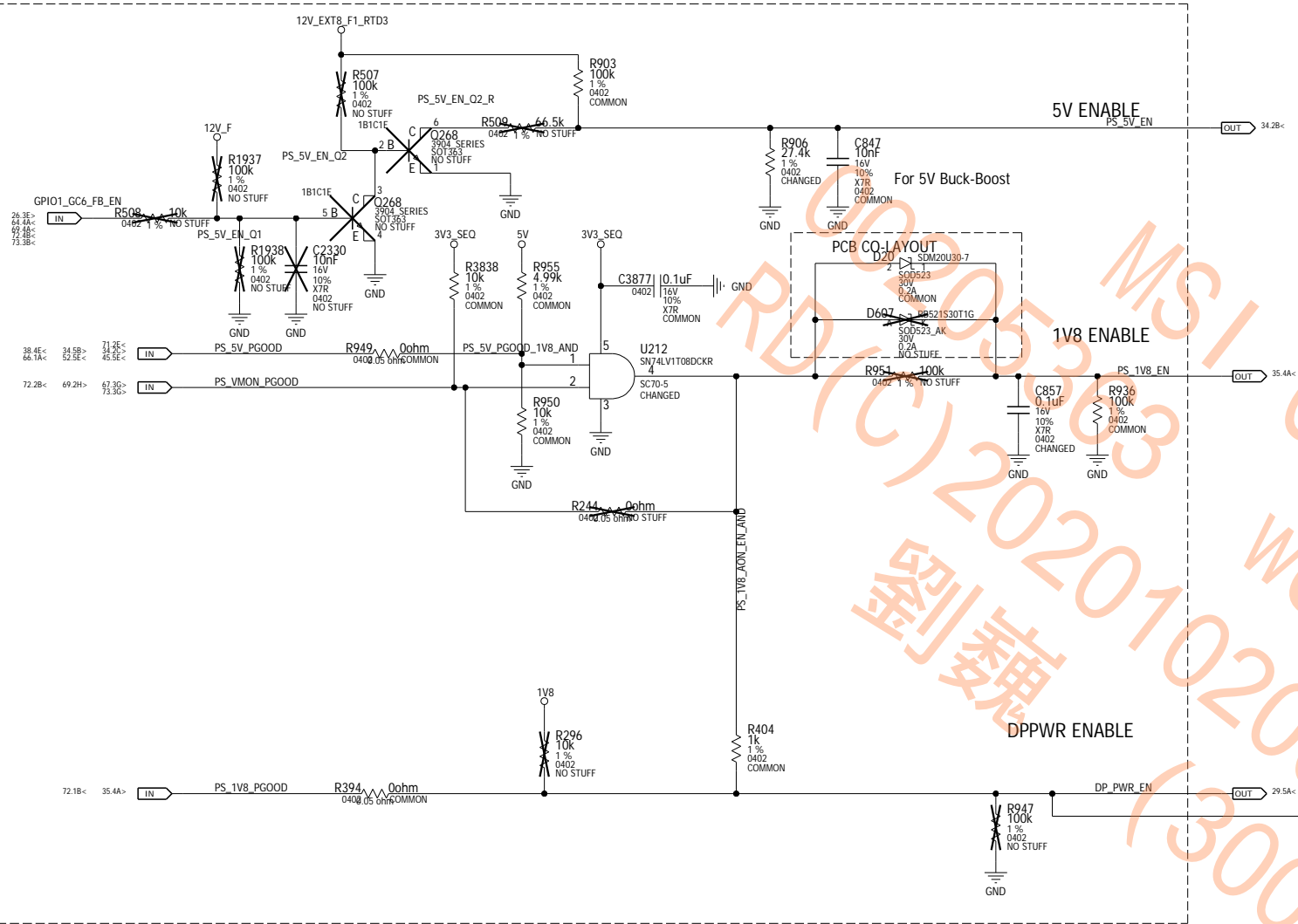
PEX12V AND 12V\_EXT8\_F2







SEQUENCE:5V,1V8,3V3\_SEQ ENABLE



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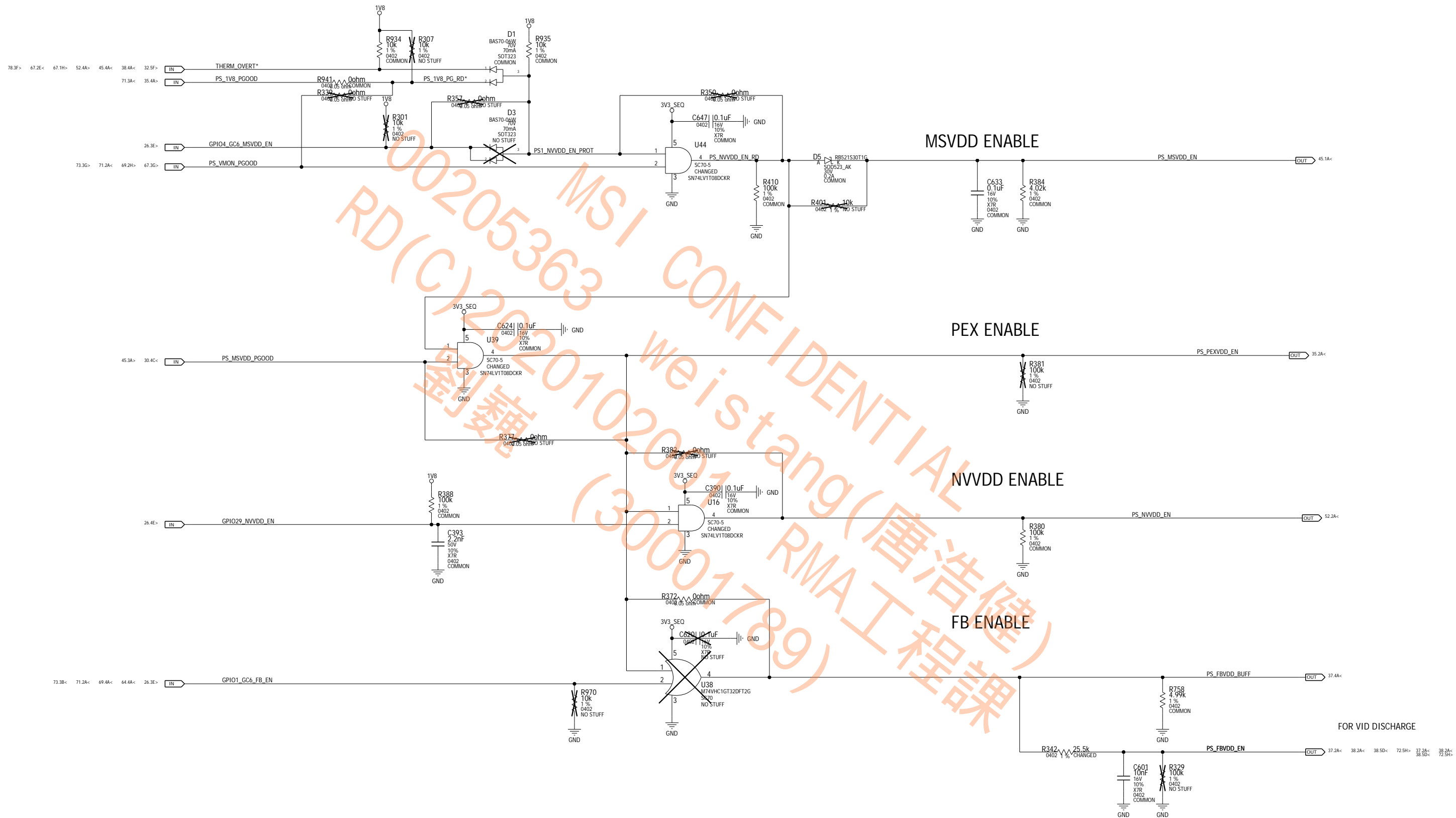
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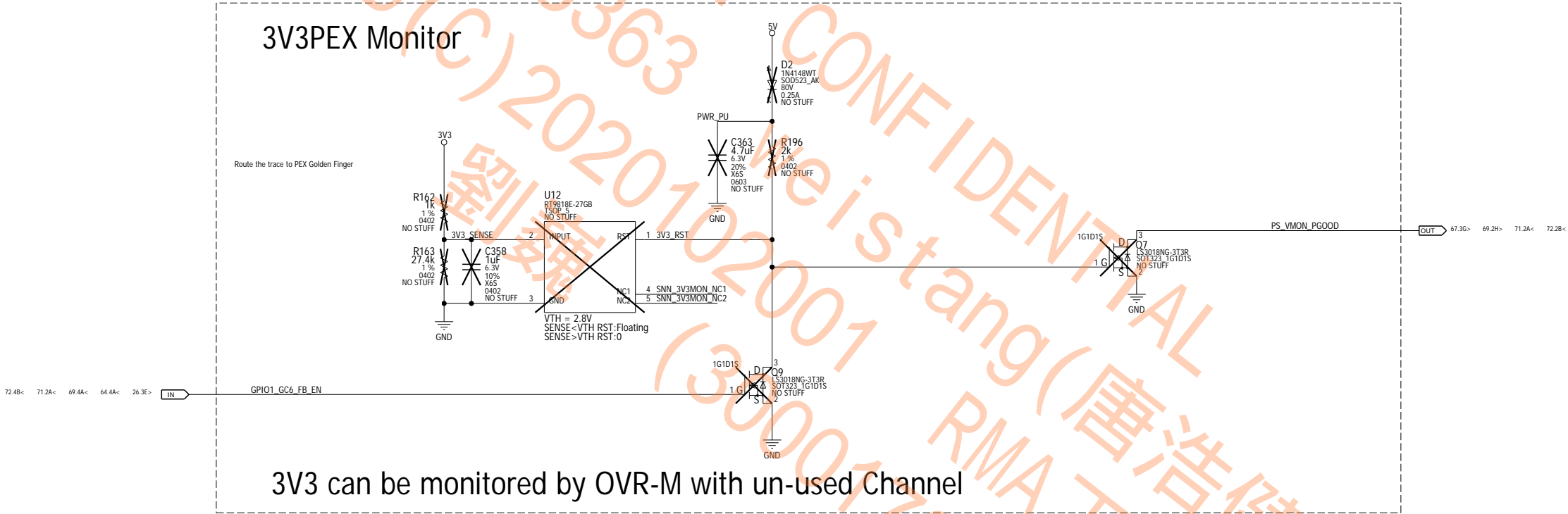
DATE 15-JUN-2020

SEQUENCE: NV, PEX, FB ENABLE



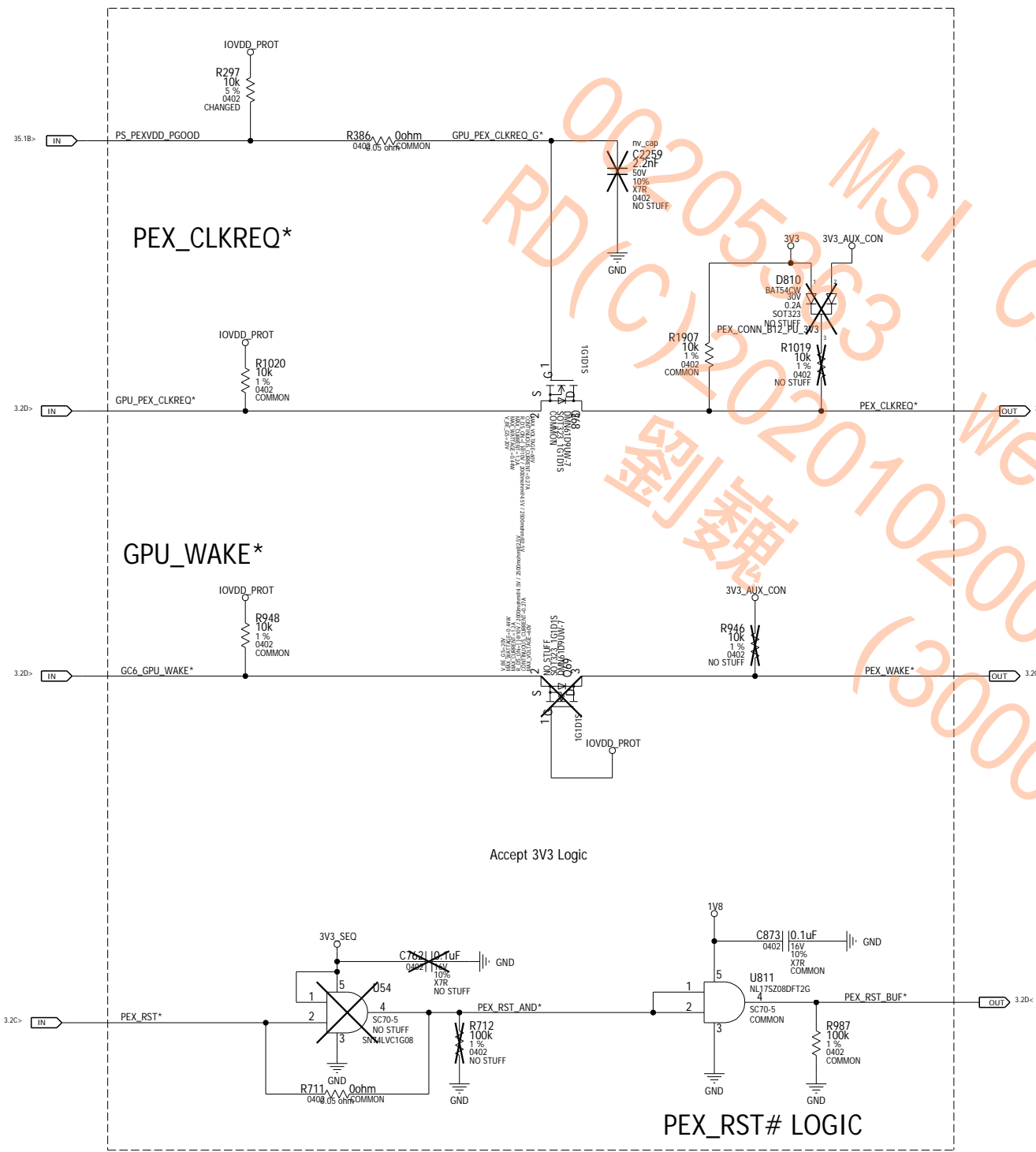
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Sequence: 3V3 MONITOR



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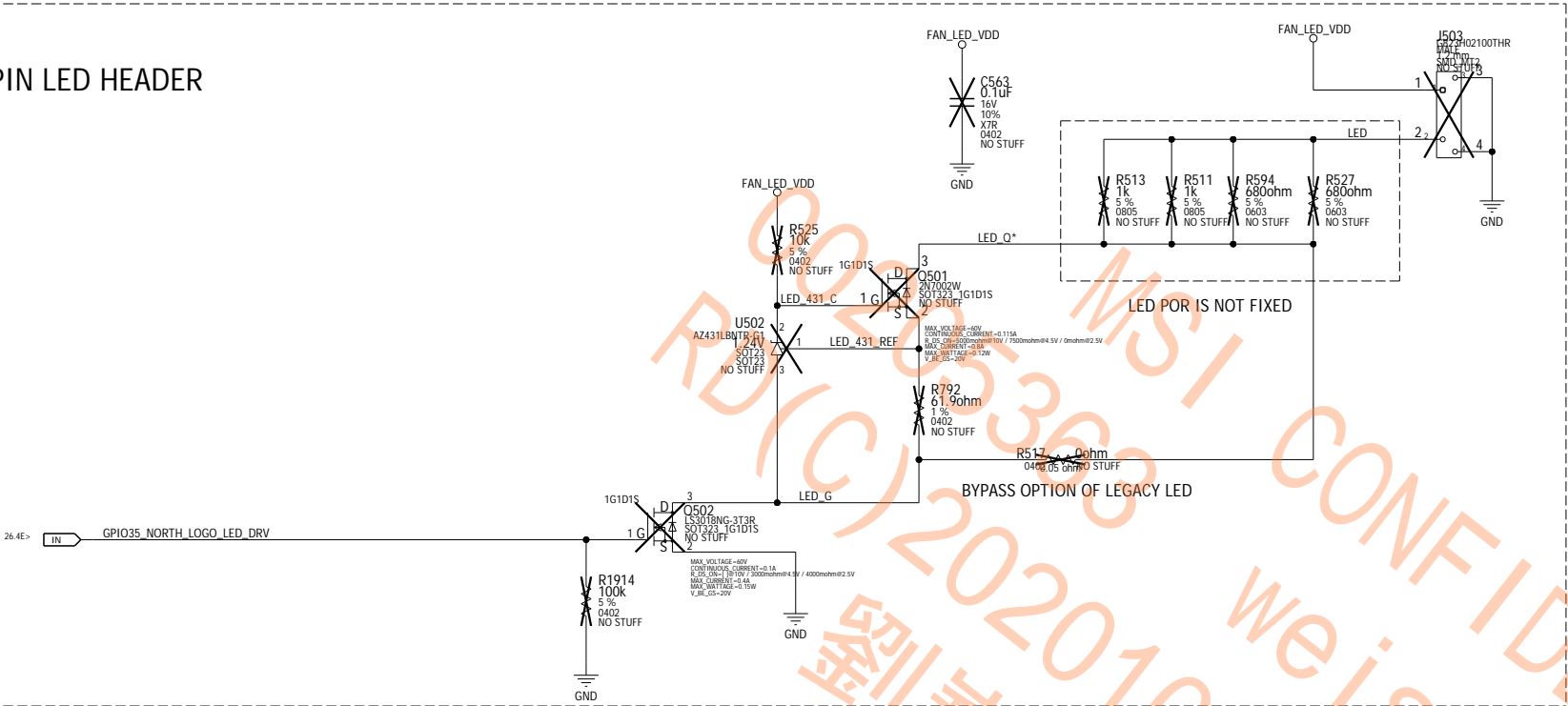
SEQUENCE:MISC



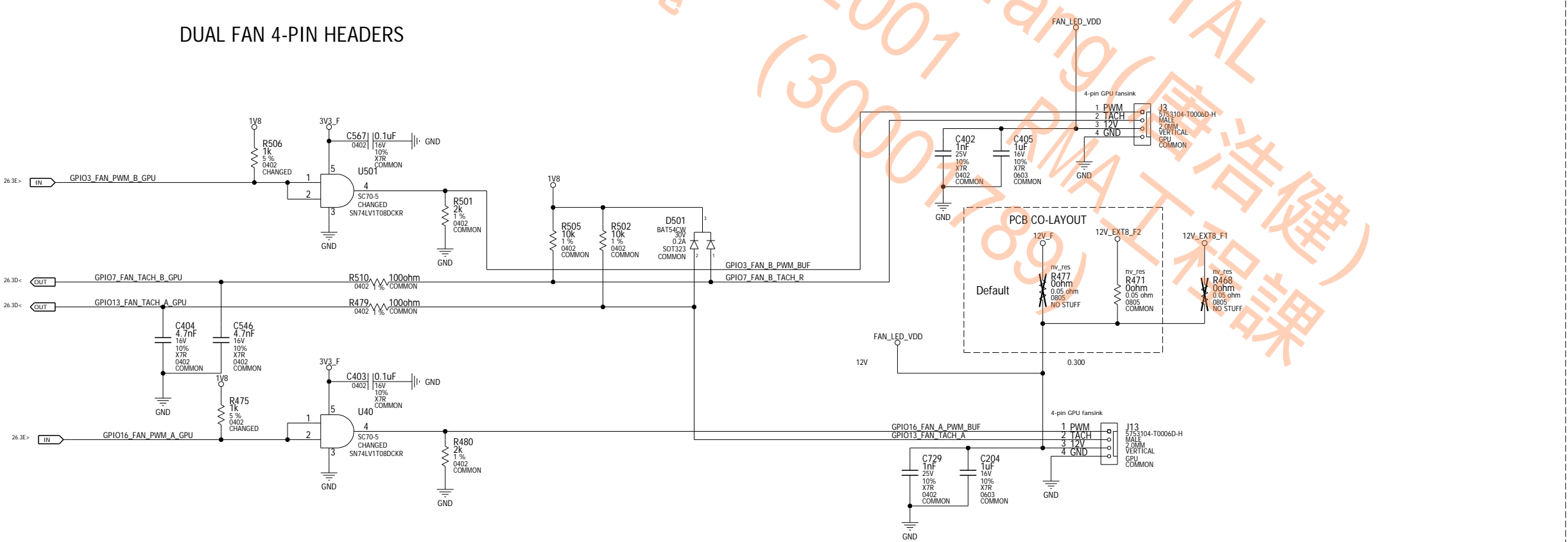
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MISC: LED & FAN

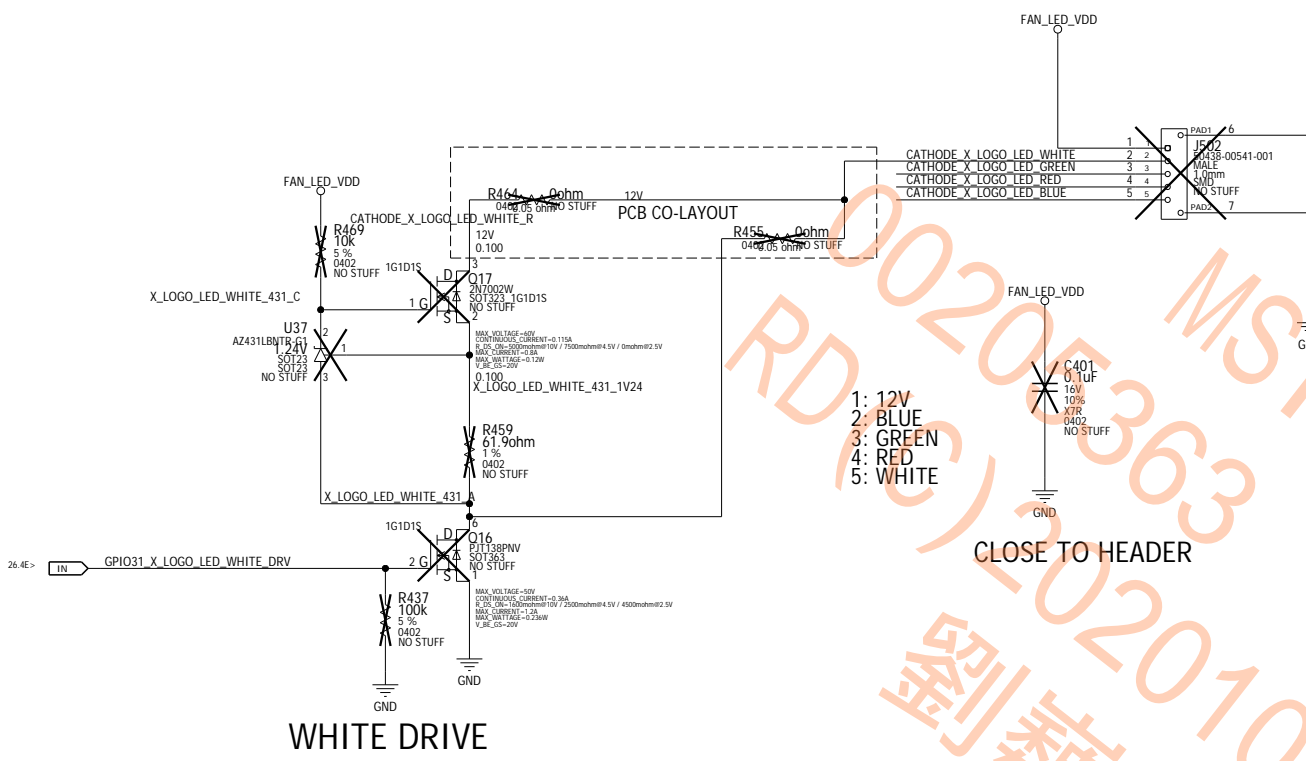
2-PIN LED HEADER



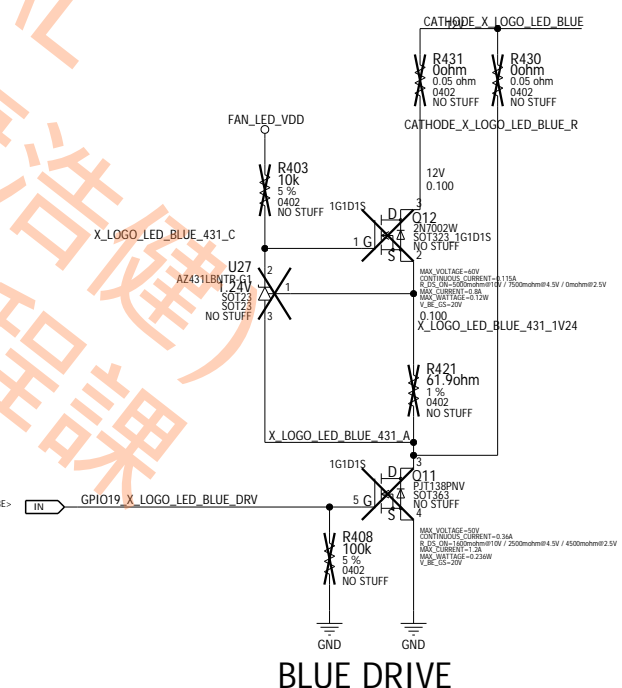
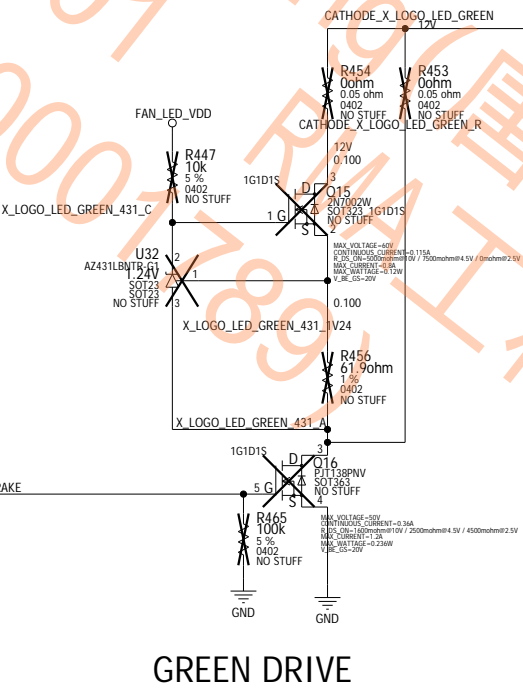
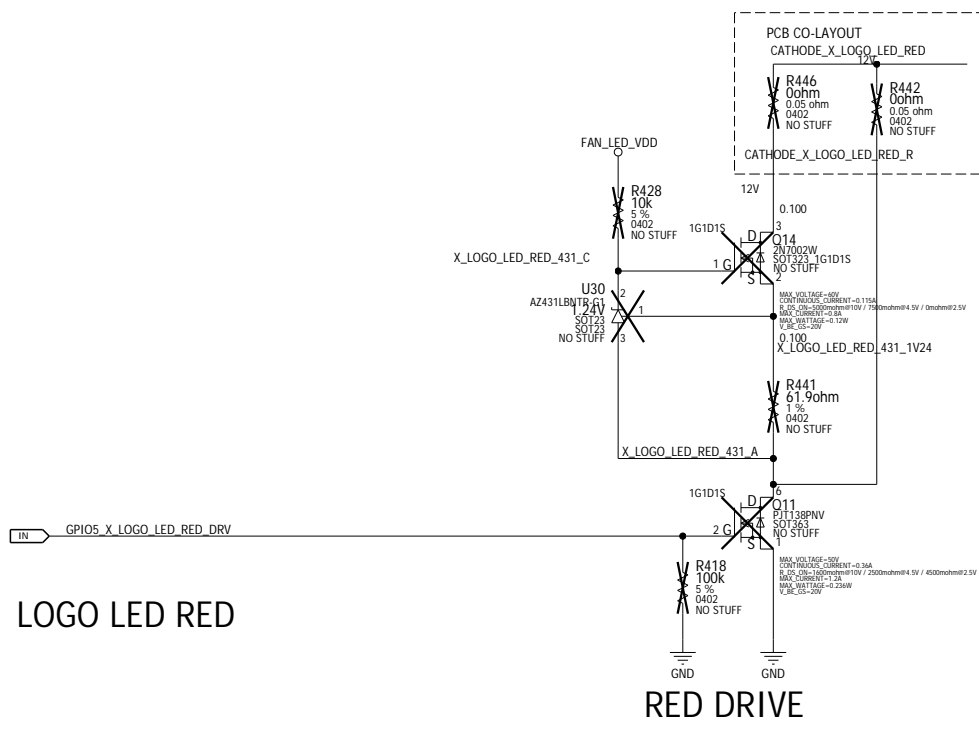
DUAL FAN 4-PIN HEADERS



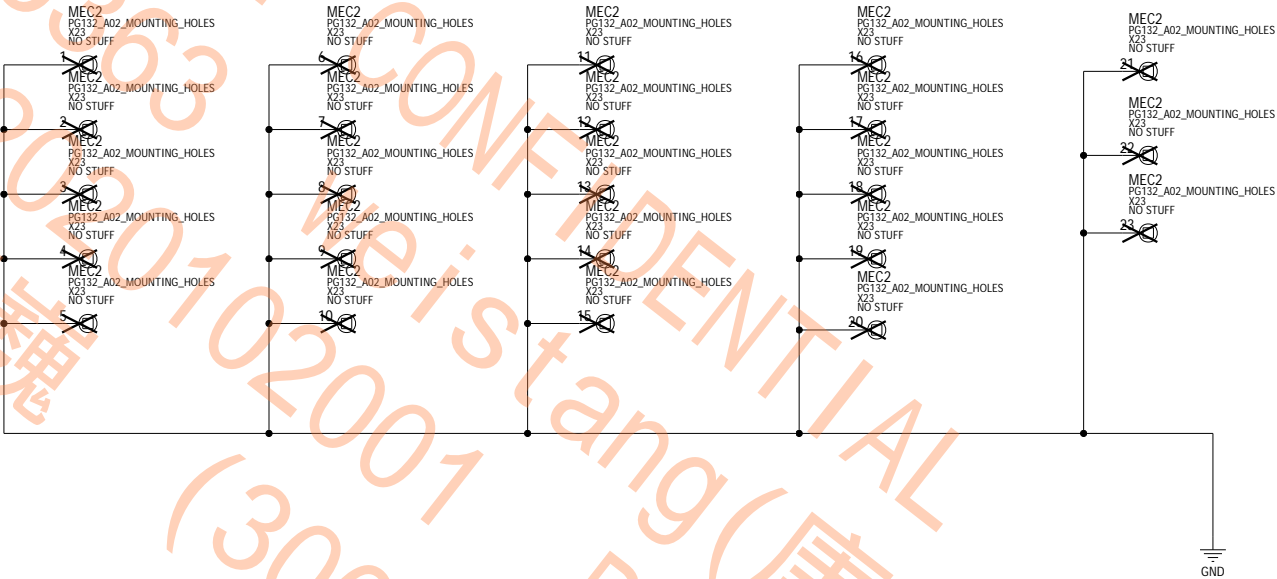
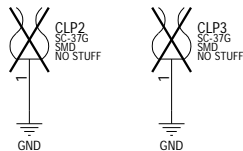
MISC: RGBW LED REF



CONNECTOR SIZE TO FIT LAYOUT  
ALL CIRCUIT NO STUFF BY DEFAULT  
ALINGN WITH PASH  
IF ADDITIONAL POWER CONSUPTION R  
NEEDED, IT SHOULD BE ON LED MOD.



Mechanical: Mounting holes

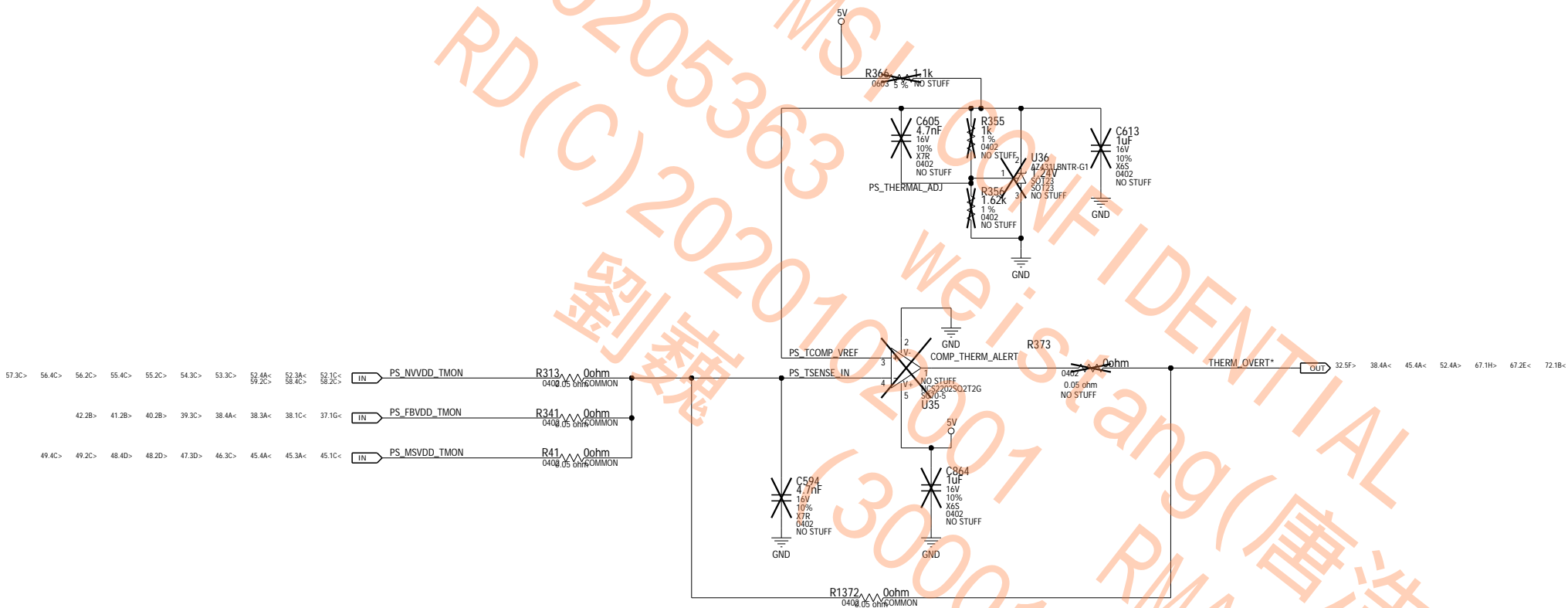


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